

66/ST/01



## Divisional Application Transmittal

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**ASSISTANT COMMISSIONER FOR PATENTS**  
**BOX PATENT APPLICATION**  
**Washington, D.C. 20231**

Sir:

Transmitted herewith for filing under 37 CFR 1.53(b) is the

patent application of  
 continuation patent application of  
 divisional patent application of  
 continuation-in-part patent application of

Inventor(s)/Applicant Identifier: David Cheung, Joe Feng, Judy H. Huang, Wai-Fan Yau

## For: METHOD AND APPARATUS FOR DEPOSITING ANTIREFLECTIVE COATING

This application claims priority from each of the following Application Nos./filing dates:  
08/567,338 filed December 5, 1995; 08/672,888 filed June 28, 1996, the disclosure(s) of which is (are) incorporated by reference.

Please amend this application by adding the following before the first sentence: "This application is a  Divisional patent application and claims the benefit of U.S. Application Serial No. 08/672,888, filed June 28, 1996 (allowed), which is a Continuation-in-Part application of Serial No. 08/567,338, filed December 5, 1995 (abandoned), the disclosures of which are incorporated by reference."

Enclosed are:

14 page(s) of specification  
 9 page(s) of claims  
 1 page of Abstract  
 7 page(s) of Preliminary Amendment.  
 A Letter to the Official Draftsperson.  
 3 sheet(s) of  formal  informal drawing(s).  
 An assignment of the invention to Applied Materials, Inc. was filed in the parent application and is still proper and desired.  
 A  signed  unsigned Declaration & Power of Attorney (from the parent).

D

	(Col. 1)	(Col. 2)
FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	21 - 20	= *1
INDEP. CLAIMS	4 - 3	= *1
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENTED		

\* If the difference in Col. 1 is less than 0, enter "0" in  
 Col. 2.

Please charge Deposit Account No. 20-1430 as follows:

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SF 1035355 v1

Attorney Docket No. AM1084D01/T9320TTC Ref No. 16301-009320"Express Mail" Label No. EM589214330USDate of Deposit: October 15, 1999

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**BOX PATENT APPLICATION**  
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By: Quinn Lamont

jc589214330US  
 09/41818

SMALL ENTITY		OTHER THAN SMALL ENTITY	
RATE	FEES	RATE	FEES
	\$380.00		\$760.00
x \$9.00 =		x \$18.00 =	\$18.00
x \$39.00 =		x \$78.00 =	\$78.00
+ \$130.00 =		+ \$260.00 =	
TOTAL		TOTAL	\$856.00

Respectfully submitted,  
 TOWNSEND and TOWNSEND and CREW LLP

Chun-Pok Leung  
 Reg No.: 41,405  
 Attorneys for Applicant

"Express Mail" Label No. EM589214330US  
Date of Deposit October 15, 1999

**PATENT**  
Attorney Docket No.: AM1084D01/T9320  
TTC Reference No.: 16301-009320

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BOX PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

By: Deanne Damonte

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

David Cheung, *et al.*

Application No.: Divisional application of  
S.N. 08/672,888

Filed: June 28, 1996

For: METHOD AND APPARATUS FOR  
DEPOSITING ANTIREFLECTIVE  
COATING

Examiner: Unassigned

Art Unit: Unassigned

**PRELIMINARY AMENDMENT**

BOX PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination of the above-referenced application, please enter the following amendments and remarks.

**IN THE SPECIFICATION:**

Page 7, line 8, please change "Si<sub>3</sub>H<sub>4</sub>" to -SiH<sub>4</sub>--.

**IN THE CLAIMS:**

Please cancel claims 11-43 without prejudice, amend claim 1, and add new claims 44-54. Note that claims 2-10 remain unamended, but are reproduced below for the Examiner's convenience and reference.

1           1. (Amended) A substrate processing system, comprising:  
2           a vacuum chamber;  
3           a substrate supporter, located within the vacuum chamber, for holding a  
4           substrate;  
5           a gas manifold for introducing process gases into the chamber;  
6           a gas distribution system, coupled to the gas manifold, for distributing the  
7           process gases to the gas manifold from gas sources;  
8           a power supply coupled between the substrate supporter and the gas manifold;  
9           a vacuum system for controlling pressure within the vacuum chamber;  
10          a controller, including a computer, for controlling the gas distribution system,  
11          the power supply and the vacuum system; and  
12          a memory coupled to the controller comprising a computer [usable] readable  
13          medium having a computer readable program code embodied therein for directing operation of  
14          the substrate processing system, the computer readable program code including:  
15            computer readable program code for causing the gas distribution system  
16            to introduce a first process gas comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into the chamber  
17            to deposit a first plasma enhanced CVD layer over the wafer; and  
18            computer readable program code for causing the gas distribution system  
19            to introduce a second process gas comprising He into the chamber to control the  
20            deposition rate of the first layer.

1           2. A substrate processing system as in claim 1 wherein the computer  
2          readable program code for causing the gas distribution system to introduce the first process gas  
3          comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into the chamber controls the introduction of the SiH<sub>4</sub> to  
4          be between 5 to 300 sccm, and the rate of N<sub>2</sub>O to be between 5 to 300 sccm.

1           3. A substrate processing system as in claim 2 wherein the computer  
2          readable program code for causing the gas distribution system to introduce a second process  
3          gas comprising He into the chamber controls the chamber pressure at about 1 to 6 torr.

1           4. A substrate processing system as in claim 3 wherein the computer  
2 readable program code for causing the gas distribution system to introduce the first process gas  
3 comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>0 into the chamber controls the introduction of the SiH<sub>4</sub> to  
4 be at a ratio of between 0.5 to 3 times the amount of N<sub>2</sub>0.

1           5. A substrate processing system as in claim 1 further comprising:  
2           computer readable program code for causing the gas distribution system to  
3 introduce a third process gas comprising NH<sub>3</sub> into the chamber; and  
4           computer readable program code for causing the gas distribution system to  
5 introduce a fourth process gas comprising N<sub>2</sub> into the chamber.

1           6. A substrate processing system as in claim 5 wherein:  
2           the computer readable program code for causing the gas distribution system to  
3 introduce a third process gas comprising NH<sub>3</sub> into the chamber controls the introduction of the  
4 NH<sub>3</sub> to be between a rate of 0 to 300 sccm; and  
5           the computer readable program code for causing the gas distribution system to  
6 introduce a fourth process gas comprising N<sub>2</sub> into the chamber controls the introduction of the  
7 N<sub>2</sub> to be between a rate of 0 to 4000 sccm.

1           7. A substrate processing system as in claim 1 further comprising computer  
2 readable program code for controlling the gas distribution system to operate for a specified  
3 time period.

1           8. A substrate processing system as in claim 7 wherein the computer  
2 readable program code for controlling the gas distribution system to operate for a specified  
3 time period comprises computer readable program code for causing the first plasma enhanced  
4 CVD layer to be formed to a thickness which is an odd multiple, greater than one, of a  
5 wavelength of light to be used in a subsequent process operation on the layer.

1           9. A substrate processing system as in claim 2 wherein the computer  
2 readable program code for causing the gas distribution system to introduce the first process gas

3 comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into the chamber controls the introduction of the SiH<sub>4</sub> to  
4 be between 15 to 160 sccm, and the rate of N<sub>2</sub>O to be between a rate of 15 to 160 sccm.

1 10. A substrate processing system as in claim 9 further comprising:  
2 computer readable program code for causing the gas distribution system to  
3 introduce a third process gas comprising NH<sub>3</sub> into the chamber at a rate of less than 150 sccm;  
4 and

5 computer readable program code for causing the gas distribution system to  
6 introduce a fourth process gas comprising N<sub>2</sub> into the chamber at a rate of less than 300 sccm.

1 44. (New) A substrate processing system, comprising:  
2 a process chamber;  
3 a substrate support, located within the vacuum chamber, for supporting a  
4 substrate;  
5 a power supply;  
6 a gas delivery system for delivering process gases into the process chamber;  
7 a controller configured to control the power supply and the gas delivery system;  
8 and

9 a memory coupled to the controller comprising a computer readable medium  
10 having a computer readable program embodied therein for directing operation of the substrate  
11 processing system, the computer readable program including a first set of computer  
12 instructions for controlling the gas delivery system to introduce selected deposition gases into  
13 the process chamber at deposited gas flow rates, a second set of computer instructions for  
14 controlling the gas delivery system to add a flow of an inert gas to the selected deposition  
15 gases at a flow rate previously determined to achieve a desired low deposition rate from a  
16 plasma enhanced reaction of the selected deposition gases, the desired low deposition rate  
17 being lower than a deposition rate using the selected deposition gases at the deposition gas  
18 flow rates with a lower flow rate of the inert gas, and a third set of computer instructions for  
19 controlling the power supply to supply power to the process chamber to produce a plasma  
20 enhanced reaction of the deposition gases in the process chamber to deposit a film at the low  
21 deposition rate.

1           45. (New) The substrate processing system of claim 44 wherein the inert  
2           gas comprises helium.

1           46. (New) The substrate processing system of claim 44 wherein the selected  
2           deposition gases comprise silane and an oxygen source.

1           47. (New) The substrate processing system of claim 44 wherein the selected  
2           deposition gases comprise silane and nitrous oxide.

1           48. (New) The substrate processing system of claim 44 wherein the selected  
2           deposition gases comprise silane and a nitrogen source.

1           49. (New) The substrate processing system of claim 44 further comprising a  
2           vacuum system for controlling pressure within the process chamber, and wherein the  
3           computer-readable program further comprises a fourth set of computer instructions for  
4           controlling the vacuum system to maintain a chamber pressure in the range of 1-6 Torr, and  
5           wherein the selected deposition gases comprise SiH<sub>4</sub> flowed into the chamber at a rate of 5-300  
6           sccm and N<sub>2</sub>O flowed into the chamber at a rate of 5-300 sccm.

1           50. (New) The substrate processing system of claim 49 further comprising a  
2           heater for heating the substrate, and wherein the computer-readable program further comprises  
3           a fifth set of computer instructions for controlling the heater to heat the substrate to a  
4           temperature in the range of 200-400°C.

1           51. (New) The substrate processing system of claim 50 wherein the  
2           substrate support is spaced from the gas distribution system at a distance in the range of 200-  
3           600 mils.

1           52. (New) The substrate processing system of claim 49 wherein the selected  
2           deposition gases further comprise NH<sub>3</sub> flowed into the chamber at a rate of less than 300 sccm,  
3           and N<sub>2</sub> flowed into the chamber at a rate of less than 4000 sccm.

1           53. (New) A substrate processing system, comprising:  
2           a process chamber;

3                   a substrate support, located within the vacuum chamber, for supporting a  
4                   substrate;  
5                   an RF power supply;  
6                   a heater;  
7                   a gas delivery system for delivering process gases into the process chamber;  
8                   a controller configured to control the power supply and the gas delivery system;  
9                   and  
10                  a memory coupled to the controller comprising a computer readable medium  
11                 having a computer readable program embodied therein for directing operation of the substrate  
12                 processing system, the computer readable program including a first set of computer  
13                 instructions for controlling the gas delivery system to flow He into the process chamber at a  
14                 selected flow rate to provide a chamber pressure in the range of 1-6 Torr, a second set of  
15                 computer instructions for controlling the RF power supply to supply power of 50-500 Watts to  
16                 the process chamber, a third set of computer instructions for controlling the heater to heat the  
17                 substrate to a temperature in the range of 200-400°C, a fourth set of computer instructions for  
18                 controlling the gas delivery system to flow SiH<sub>4</sub> at a flow rate of 5-300 sccm into the process  
19                 chamber, and a fifth set of computer instructions to flow N<sub>2</sub>O at a flow rate of 5-300 sccm into  
20                 the process chamber, wherein a ratio of the selected flow rate of He to the combined flow rate  
21                 of SiH<sub>4</sub> and N<sub>2</sub>O is at least 6.25:1 to deposit an antireflective layer on the substrate at a  
22                 deposition rate which is lower than a deposition rate using the same flow rate of SiH<sub>4</sub> and the  
23                 same flow rate of N<sub>2</sub>O with a lower flow rate of He.

1                 54. (New) A substrate processing system, comprising:  
2                   a process chamber;  
3                   a substrate support, located within the vacuum chamber, for supporting a  
4                   substrate;  
5                   a power supply;  
6                   a gas delivery system for delivering process gases into the process chamber;  
7                   a controller configured to control the power supply and the gas delivery system;  
8                   and

9                   a memory coupled to the controller comprising a computer readable medium  
10                  having a computer readable program embodied therein for directing operation of the substrate  
11                  processing system, the computer readable program including a first set of computer  
12                  instructions for controlling the gas delivery system to flow selected deposition gases into the  
13                  process chamber at deposition gas flow rates, a second set of computer instructions for  
14                  controlling the gas delivery system to add a flow of an inert gas to the selected deposition  
15                  gases at a flow rate previously determined to achieve a desired low deposition rate from a  
16                  reaction of the selected deposition gases, the desired low deposition rate being lower than a  
17                  deposition rate using the selected deposition gases at the deposition gas flow rates with a lower  
18                  flow rate of the inert gas, and a third set of computer instructions for controlling the power  
19                  supply to supply power to the process chamber to react the deposition gases to deposit a film at  
20                  the low deposition rate.

REMARKS

Claims 1-10 and 44-54 are pending. Claim 1 has been amended and new claims 44-54 have been added. No new matter has been introduced.

Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-576-0200.

Respectfully submitted,



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SF 1031467 v1

# PATENT APPLICATION

## Method and Apparatus for Depositing Antireflective Coating

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(single-wavelength) light, which enables them to produce very fine patterns. As repeated process steps are carried out, however, the topology of the upper surface of the substrate becomes progressively less planar. This uneven topology can cause reflection and refraction of the monochromatic light, resulting in exposure of some of the photoresist beneath the opaque portions of the mask. As a result, this differing local substrate surface typography can alter the fine patterns of photoresist, thereby changing the desired dimensions of the resulting regions of the semiconductor substrate.

In the manufacture of semiconductor devices, it is desirable that fluctuations in line width, or other critical dimensions, be minimized. Errors in such dimensions can result in open or short circuits, thereby ruining the resulting semiconductor devices. As a result, some semiconductor manufacturers now require that the dimensional accuracy of a photoresist pattern be within 5 percent. To achieve that dimensional accuracy, two approaches have been taken. Both approaches entail the use of another layer in addition to the photoresist layer.

The first approach uses a relatively thick organic film beneath the photoresist that absorbs incident light so that minimal reflection or refraction occurs. A disadvantage of such organic films is that they require more process steps, and being polymer-based, are difficult to etch.

A second approach is the use of an antireflective film for canceling reflections occurring at the photoresist-antireflective layer interface, and at the antireflective layer-substrate interface. In the prior art, silicon oxynitride (SiON) deposited using NH<sub>3</sub> gas has been used as an antireflective film. Upon exposure to light, however, an amino group from the SiON film reacts with the light sensitive component in the photoresist, thereby desensitizing the photoresist. This results in inaccurate photoresist patterns.

An article entitled "SiOxNy:H, high performance antireflective layer for the current and future optical lithography," SPIE, Vol. 2197 (1994), pp. 722-732, by Tohro Ogawa, *et al.*, addresses the thin film interference concerns. The article teaches the use of an antireflective layer (ARL) in conjunction with the I-line, KrF, and ArF excimer laser lithographies. The exposure wavelengths used in these laser lithographies are 365 nm, 248 nm, and 193 nm, respectively. The article describes that as exposure

PATENT  
16301-93 -- AM1084/P1

## 5 Method and Apparatus for Depositing Antireflective Coating

## CROSS REFERENCE TO RELATED APPLICATION

This is a continuation-in-part patent application of Application Serial No. 08/567,338, filed December 5, 1995, entitled "Anti-Reflective Coating and Method for Depositing Same."

## BACKGROUND OF THE INVENTION

The present invention relates to apparatus for, and the processing of, semiconductor wafers. In particular, the invention relates to the deposition of antireflective layers during wafer processing.

In the manufacture of integrated circuits, photolithographic techniques are used to define patterns for layers in an integrated circuit. Typically, such photolithographic techniques employ photoresist or other light-sensitive material. In conventional processing, the photoresist is first deposited on a wafer, and then a mask having transparent and opaque regions which embody the desired pattern, is positioned over the photoresist. When the mask is exposed to light, the transparent portions allow light to expose the photoresist in those regions, but not in the regions where the mask is opaque. The light causes a chemical reaction to occur in the exposed portions of photoresist. A suitable chemical, or a chemical vapor or ion bombardment process, then is used to selectively attack either the reacted or unreacted portions of the photoresist. With the photoresist pattern remaining on the wafer itself now acting as a mask for further processing, the integrated circuit can be subjected to additional process steps. For example, material may be deposited on the circuit, the circuit may be etched, or other known processes carried out.

In the processing of integrated circuit devices with small feature sizes, for example, feature sizes having critical dimensions less than one-half micron, sophisticated techniques involving equipment known as steppers, are used to mask and expose the photoresist. The steppers for such small geometry products generally use monochromatic

wavelengths become shorter, stronger reflections from the interface between the photoresist and the substrate result. Hence, an ARL is needed to reduce the standing waves and thin film interference effects.

This ARL is described as canceling reflection from both the interface between the photoresist and the ARL, and from the interface between the ARL and the substrate. The article describes a complicated equi-energy contour-based procedure for determining the parameters to achieve the desired cancellation. According to the procedure described in this article by Sony, the parameters are obtained by finding common regions of the equi-energy contour lines for a plurality of photoresist film thicknesses. The article describes refractive index, absorptive index, and thickness values for its ARL, and though the article does not specify it, Applied Materials inventors have determined that these values correspond to a phase shift of 180° between the reflections. The Applied engineers, however, were unable to achieve the results described in the article, and it is believed that the process is unstable.

Sony has also filed a European patent application (Application No. 93113219.5, Publication No. EP 0 588 087 A2) for a process for depositing an ARL with selected parameters. The Sony application discusses the SiH<sub>4</sub> and N<sub>2</sub>O ratio, and how the ratio affects the optical and chemical properties of the ARL deposited. The Sony application also teaches the use of argon.

#### SUMMARY OF THE INVENTION

The present invention provides apparatus and a process for depositing an antireflective layer. Because ARL films are thin, to have a stable process it is desirable to have a low deposition rate for the process. The invention provides apparatus and a process to lower the deposition rate of plasma-enhanced silane oxide, silane oxynitride, and silane nitride processes. In a preferred embodiment helium is used. Although helium is a known carrier gas in chemical vapor deposition, its use in the present invention is for controlling the deposition rate of the processes. By adding helium, more precise control of the thin film thickness is provided, particularly over longer periods of equipment operation. The helium also helps stabilize the process, enabling different films to be deposited, and the film deposited to be well-controlled.

The present invention also provides equipment and process conditions under which parameters can be controlled to produce ARLs with various optimum refractive index, absorptive index, and thickness values for obtaining the desired cancellations for the different exposure wavelengths and substrates. In one embodiment, the apparatus and process described by the present invention use  $N_2$  and  $NH_3$ , in addition to a desired ratio of  $SiH_4$  to  $N_2O$ , to further control the optical and chemical properties of the ARL deposited. The effects of  $N_2$  and  $NH_3$  are particularly dominant in process regimes where  $SiH_4$  and  $N_2O$  have minimal or no effect on the ARL properties, e.g., at low temperature. The invention teaches the addition of  $NH_3$  and  $N_2$  in the process to change the composition of the film, allowing more freedom and finer tuning of the refractive index and the absorptive index. Furthermore, the process is compatible with the use of helium, which is more cost-effective than argon. Helium also allows for improved stress control of the ARL layer deposited. This helps prevent the film from becoming too tensile, which can cause it to flake off the substrate after deposition.

As applied to the process above, the addition of helium also achieves plasma stability, which in turn ensures the deposition of a uniform film. Furthermore, the helium provides sufficient control of the ARL deposition process, so that ARLs with optimum values of refractive index, absorptive index, and thickness can be developed within practical process parameters for exposure wavelengths in the range of 190-900 nm. This is highly desirable because cancellation of the reflected light for different exposure wavelengths depends on several factors: the incident light wavelength, the phase shift (which is determined by the thickness of the ARL), and the intensity of the reflection (determined by the chemical composition of the ARL). Hence, control of the optical and chemical properties of the ARL is necessary to achieve the desired cancellations.

The present invention determines the optimum refractive index, absorptive index, and thickness values for phase shifts greater than  $180^\circ$  (e.g.,  $540^\circ$ ,  $900^\circ$ , etc.) using destructive interference equations. (A sample calculation of how the refractive index  $n$ , absorptive index  $k$ , and thickness  $t$  values are determined by the current invention is shown below.) For an ARL to produce phase shifts of  $540^\circ$  or larger between the reflections, the thickness will be higher, which means higher absorptive index values, because the ARL must absorb more refracted light. In one embodiment, the invention

provides an ARL with optimum refractive index  $n$ , optimum absorptive index  $k$ , and optimum thickness  $t$  values to produce a phase shift of  $540^\circ$  between the reflections.

#### BRIEF DESCRIPTION OF THE DRAWINGS

5       Figure 1 is a vertical, cross-sectional view of one embodiment of a simplified chemical vapor deposition (CVD) apparatus used for processing the antireflective coating according to the present invention;

10       Figure 2 is a vertical, cross-sectional view of paths of reflected and refracted light of an incident light beam which strikes the surface of a multilayer semiconductor device, for example, during a photolithographic process;

15       Figure 3 shows the effect of using an antireflective layer according to the present invention; and

20       Figure 4 is a trend chart for the process of depositing the antireflective layer of the present invention.

#### DESCRIPTION OF THE SPECIFIC EMBODIMENTS

25       A preferred embodiment of the process for depositing an ARL according to the present invention comprises apparatus for and the use of plasma-enhanced chemical vapor deposition (PECVD) technique to bring about a chemical reaction between  $\text{SiH}_4$  and  $\text{N}_2\text{O}$ , in the presence of He, with the  $\text{SiH}_4$  to  $\text{N}_2\text{O}$  ratio being between about 0.5 to 3.0 and preferably 1.0. The process further includes the addition of  $\text{NH}_3$ ,  $\text{N}_2$ , and He gases. Figure 1 illustrates one embodiment of a simplified, parallel plate PECVD system 10 having a vacuum chamber 15 in which the antireflective layer can be deposited according to the present invention.

30       System 10 contains a gas distribution manifold 11 for dispersing the deposition gases to a substrate, not shown, but which is placed flat on the supporter 12. The supporter 12 is highly heat-responsive and is mounted on supports 13 so that supporter 12 (and the substrate supported on the upper surface of supporter 12) can be controllably moved between a lower loading/offloading position, and an upper processing position 14 represented by a dashed line, which is closely adjacent to manifold 11.

Depending on the desired refractive index, absorptive index, and thickness to be achieved, the spacing between supporter 12 and manifold 11 is in the range of 200-600 mils, the substrate temperature is in the range of 200-400°C, and the chamber pressure is maintained at 1-6 torr. ARLs with varying refractive index, absorptive index, and thickness can be deposited within these process parameters for any exposure wavelengths between 190-900 nm; and the different optimum refractive index, absorptive index, and thickness for the different wavelengths can be consistently achieved by varying the parameters and the rate at which the SiH<sub>4</sub>, N<sub>2</sub>O, NH<sub>3</sub>, N<sub>2</sub>, and He gases are introduced into the chamber. Within these ranges, the preferred range for the spacing is 400-600 mils. For the substrate temperature, the preferred range is 300-400°C, and the preferred range for the chamber pressure is 4.5-5.5 torr.

When supporter 12 and the wafer are in processing position 14, they are surrounded by a baffle plate 17 having a plurality of spaced holes 23 which allow gas to exhaust into an annular vacuum manifold 24. Deposition gases are supplied through lines 18, having control valves (not shown), into a gas mixing chamber 19 where they are combined and supplied to manifold 11. Although He is a known carrier gas, its use in the present process is for controlling the parameters of the process. As will be described later, the amount of He used affects the optical and chemical properties of the ARL deposited. Furthermore, He helps achieve the desired chamber pressure without altering the chemical composition of the film, thereby ensuring process stability, which in turn ensures the deposition of a uniform film. Because the ARL is a thin film, thickness control is important, and a low deposition rate is necessary to achieve control over the desired thickness. The addition of He lowers the deposition rate, thus allowing for thickness control, in addition to the control of the film properties.

During processing of a wafer, gas inlet to manifold 11 is vented toward, and uniformly distributed radially across, the surface of the substrate as indicated by arrows 22 and 21 representing gas flow. SiH<sub>4</sub> and N<sub>2</sub>O are both introduced into chamber 19 at a rate of 5-300 sccm, with the SiH<sub>4</sub> to N<sub>2</sub>O ratio between about 0.5 and 3.0, but preferably about 1.0. NH<sub>3</sub>, N<sub>2</sub>, and He may be added as explained below, depending on the values of refractive index, absorptive index, and thickness desired, and the process regimes. If a wider range of refractive index, absorptive index, and thickness values is

desired, NH<sub>3</sub>, N<sub>2</sub>, and additional He will be added to the process, and are introduced into the chamber at a rate of 0-300 sccm, 0-4000 sccm, and 5-5000 sccm, respectively. Within these ranges, the preferred range for introducing SiH<sub>4</sub> into the chamber is 15-160 sccm; for N<sub>2</sub>O, the preferred range is 15-160 sccm; for NH<sub>3</sub>, the preferred range is 0-300 sccm; for N<sub>2</sub>, the preferred range is 0-500 sccm; and for He, the preferred range is 500-4000 sccm. After the reactions are complete, the remaining gases are exhausted via ports 23 into the circular vacuum manifold 24 and out through an exhaust line (not shown). The optimal values for the gases are Si<sub>3</sub>H<sub>4</sub> 40-120 sccm, N<sub>2</sub>O 30-120 sccm, He 1500-2500 sccm, N<sub>2</sub> 0-300 sccm, and NH<sub>3</sub> 0-150 sccm. These are representative values for an eight-inch chamber as made by Applied Materials. Other sizes of chambers or chambers made by other manufacturers will have different values.

A controlled plasma of SiH<sub>4</sub> and N<sub>2</sub>O is formed adjacent to the substrate by RF energy applied to manifold 11 from RF power supply 25. Gas distribution manifold 11 is also an RF electrode, while supporter 12 is grounded. The RF power supply 25 supplies power ranging from 50-500 watts, to manifold 11 to slow down or enhance the decomposition of the SiH<sub>4</sub> and N<sub>2</sub>O introduced into chamber 15.

A circular external lamp module 26 provides a collimated annular pattern of light 27 through quartz window 28 onto supporter 12. Such heat distribution compensates for the natural heat loss pattern of the supporter and provides rapid and uniform supporter and substrate heating for effecting deposition. A motor, not shown, raises and lowers supporter 12 between a processing position 14 and a lower, substrate-loading position.

The motor, control valves connected to lines 18, and RF power supply 25 are controlled by a processor 34 over control lines 36, of which only some are shown. Using these control lines, the processor controls the entire process of depositing the ARL. Processor 34 operates under the control of a computer program stored in a memory 38. The computer program dictates the timing, mixture of gases, chamber pressure, chamber temperature, RF power levels, supporter position, and other parameters of the process. Typically, the memory contains computer readable information for causing the processor to introduce a first process gas comprising SiH<sub>4</sub> and N<sub>2</sub>O into the chamber, and a second process gas comprising He into the chamber.

The above description is mainly for illustrative purposes and should not be considered as limiting the scope of the present invention. Variations of the above described system such as variations in supporter design, heater design, location of RF power connections, etc., are possible. Additionally, other plasma CVD equipment such as electron cyclotron resonance (ECR) plasma CVD equipment, induction-coupled RF high density plasma CVD equipment, or the like may be employed. The ARL and method for forming such a layer of the present invention is not limited to any specific apparatus or to any specific plasma excitation method.

Similarly, the use of helium for controlling the deposition rate of the process and for stabilizing the process, is applicable to thin film depositions in general, and is not limited to ARL film depositions. Specifically, it could be used to lower the deposition rate of existing plasma-enhanced silane oxide, silane oxynitride, and silane nitride processes. Although helium is used in the preferred embodiment, other inert gases may also be used instead of helium.

Figure 2 is a vertical, cross-sectional view of typical paths of reflected and refracted light of an incident light beam that strikes the surface of a multi-layer semiconductor device during a photolithographic process. As shown in Figure 2, for an incident light beam 1 that strikes the semiconductor structure, the photoresist pattern exposure could be distorted by a reflection 3 between the photoresist layer and an underlying layer, and another reflection 6 between the underlying layer and the substrate which results in light 5 entering the photoresist.

In Figure 3, the dashed light paths illustrate the function of an ARL according to the present invention. As shown, light rays 3 and 5 (which are almost equal in intensity and have a phase difference of 540° or larger) will substantially cancel each other, while light rays 4 and 6 will be absorbed by the ARL. Hence, the only light that exposes the photoresist is the incident light from ray 2. As previously mentioned, an ARL according to the present invention is compatible with the photoresist, thus eliminating the concern that the photoresist may be neutralized. Furthermore, as discussed below, ARLs with different optimum refractive index, absorptive index, and thickness values can be achieved for cancellation of reflections of different exposure wavelengths between 190-900 nm.

Figure 4 is a chart showing the effects of the different process parameters on the process for depositing the antireflective layer of the present invention. As described, the properties of the ARL can be changed by changing the different parameters. As shown by the chart, increasing the substrate temperature will increase the refractive index  $n$ , absorptive index  $k$ , thickness  $t$ , and reflectance  $r$  values of the ARL deposited. Similarly, increasing the total gas flow into the chamber, or increasing the rate at which  $\text{SiH}_4$  is introduced into the chamber, will also increase the refractive index  $n$ , absorptive index  $k$ , thickness  $t$ , and reflectance  $r$  values of the ARL deposited.

On the other hand, increasing the pressure of chamber 19, or the spacing between supporter 12 and manifold 11, has the effect of decreasing the refractive index  $n$ , absorptive index  $k$ , thickness  $t$ , and reflectance  $r$  values of the ARL deposited. Alternatively, increasing the power supplied to RF power supply 25 to generate more plasma has the effect of decreasing the refractive index  $n$ , absorptive index  $k$ , and reflectance  $r$  values while increasing the thickness of the ARL deposited. A similar effect can also be achieved by increasing the rate at which  $\text{N}_2\text{O}$  or  $\text{N}_2$  is being introduced into chamber 19. The opposite effect of increasing the refractive index  $n$ , absorptive index  $k$ , and reflectance  $r$  values, while decreasing the thickness of the ARL deposited, can be achieved by increasing the rate at which He is introduced into chamber 19. Finally, the amount of  $\text{NH}_3$  introduced into chamber 19 can be increased to increase the refractive index  $n$  and thickness  $t$  values, while decreasing the absorptive index  $k$  and reflectance  $r$  values.

The following discussion of the ARL explains the calculations below. These calculations pertain to the deposition of SiON films by plasma-enhanced CVD techniques, an example of which is as described above. The values obtained from the calculations are for an exposure wavelength of approximately 248 nm. At this wavelength, an ARL deposited in this process can have refractive index  $n$  ranging from 1.7 to 2.4, and absorptive index  $k$  ranging from 0 to 1.3.

An effective ARL minimizes the variation of light available for PR absorption as the thickness of the PR varies. This requires the substantial cancellation of light reflected from the interface between the PR and the ARL, i.e., the substantial cancellation of light rays 3 and 5 as shown in Figure 3. Substantial cancellation can be

achieved if the following two requirements are met simultaneously for light ray 3 and light ray 5. The phase difference between ray 3 and ray 5 is close to an odd multiples of  $180^\circ$ .

$$n_3 \cdot 2t = \frac{1}{2} (m\lambda) \quad (1)$$

The intensity of ray 3 and the intensity of ray 5 are almost identical.

$$I_3 = I_5 \quad (2)$$

The first of the above requirements is described by the destructive interference equation, which is represented by Equation 1. The second equation describes the condition for matching the intensities of ray 3 and ray 5.

For a given substrate and photoresist, conditions 1 and 2, as represented by Equations 1 and 2, can be satisfied simultaneously with appropriate choices of refractive index  $n$ , absorptive index  $k$ , and thickness  $t$  of the ARL film. Solutions for  $m=3$  ( $540^\circ$  phase difference),  $m=5$  ( $900^\circ$  phase difference) and higher odd multiples of  $180^\circ$  phase difference require a larger ARL thickness to satisfy Equation 1 as compared to the  $m=1$  case. Because of the larger ARL thickness, different refractive index, and absorptive index values are needed to satisfy Equation 2. These different refractive index, and absorptive index values can be achieved by the present invention for ARL thicknesses in the range of 500-3000 angstroms.

It is desirable to achieve refractive index and absorptive index values for thicker ARLS, because the increased thickness can be a major advantage in the manufacturing of an ARL for achieving film consistency from wafer to wafer. Because a thicker film requires a longer deposition time compared to a thinner film, the refractive index, absorptive index, and thickness of the film can be better controlled. For example, film thickness suggested by the SONY researchers is near 250 angstroms. Assuming a deposition rate of 2000 angstroms per minute, the deposition time would be 7.5 seconds, including the time for striking the plasma and extinguishing the plasma. These are not well-controlled regions of the deposition process, and generally contribute to wafer-to-wafer variation in thickness and film properties, including refractive index and

absorptive index. If a film thickness of 750 angstroms can be used, the film deposition time is increased to 22.5 seconds and the fractional contribution of deposition time from striking and extinguishing the plasma is reduced threefold. This reduction in fractional contribution to deposition time from striking and extinguishing the plasma, substantially improves the wafer-to-wafer variation of refractive index, absorptive index, and thickness compared with the 250 angstroms film. This concept is not limited to  $m=3$ . It is valid for 5, 7, and all odd multiples of  $180^\circ$  phase difference of the solutions for Equation 1 and 2.

For applications requiring a hard mask, the ARL film left on the substrate can be used as hard mask during an etching step. This is possible if the thickness of the film is sufficient to allow for etching of the substrate without completely eroding the ARL layer. Hence, this is another potential advantage of using a thicker ARL film with refractive index and absorptive index values that correspond to higher odd multiple solutions to Equations 1 and 2.

Appendix A provides examples of refractive index  $n$ , absorptive index  $k$  and thickness  $t$  values, which correspond to solutions for  $540^\circ$  phase difference for Equations 1 and 2 at 248 nm with SiON. The refractive index  $n$  and the absorptive index  $k$  for the photoresist are assumed to be 1.80 and 0.011, respectively. The values of refractive index  $n$  and absorptive index  $k$  for Al, Al-Si, Al-Si-Cu, and Al-Cu are shown. Finally, the values of refractive index  $n$  and absorptive index  $k$  for W-Si are assumed to be 1.96 and 2.69, respectively. With two equations and three unknowns, one can choose the value of refractive index  $n$ , absorptive index  $k$ , or thickness  $t$  and then calculate the remaining two unknowns. Because the refractive index  $n$  value of SiON ARL films optimized with the above deposition process is about 2.2 to 2.3 at 248 nm, the refractive index  $n$  value is chosen to stay within this range throughout the calculation of Appendix A. Because absorptive index  $k$  values are tunable over a wide range with the above process, values of absorptive index  $k$  are not restricted.

The solutions provided in Appendix A are not exact optimized values for the ARL because of the simplicity of the model. For example, native oxides of Al, or W-Si, are simply neglected and their thicknesses are usually in the range of 10 to 20 angstroms. Also, the refractive index  $n$  and absorptive index  $k$  values of the ARL film

are assumed to be constant throughout the thickness of the film. Thus, the solutions to Equations 1 and 2 only provide a guideline to the refractive index  $n$ , absorptive index  $k$ , and thickness of the desirable ARL film. Exact values of refractive index  $n$ , absorptive index  $k$ , and thickness for a specific application are determined experimentally by optimizing near the solution values from Equations 1 and 2.

For an Al substrate with deep UV (248 nm) photolithography, at  $m=3$  an appropriate refractive index  $n$  value is about 2.3, an appropriate absorptive index  $k$  value is about 0.3 and an appropriate thickness value is about 800 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $540^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity. For a W-Si substrate with deep UV photolithography, at  $m=3$ , an appropriate refractive index  $n$  is about 2.3, an absorptive index  $k$  is about 0.3 and a thickness about 800 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $540^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is less than 5 percent of incident intensity.

For an Al substrate with deep UV photolithography, at  $m=5$  an appropriate refractive index  $n$  value is about 2.3, an appropriate absorptive index  $k$  value is about 0.17 and an appropriate thickness value is about 1350 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $900^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity. For a W-Si substrate with deep UV photolithography, at  $m=5$ , an appropriate refractive index  $n$  is about 2.3, an absorptive index  $k$  is about 0.18 and a thickness about 1350 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $900^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is less than 5 percent of incident intensity.

For an Al substrate with deep UV photolithography, at  $m=7$  an appropriate refractive index  $n$  value is about 2.3, an appropriate absorptive index  $k$  value is about 0.13 and an appropriate thickness value is about 1900 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $1260^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity. For a W-Si substrate with deep UV photolithography, at  $m=7$  an appropriate refractive index  $n$  is about 2.3, an absorptive index  $k$  is about 0.13 and a thickness about 1900 angstroms.

These solutions satisfy Equation 1 to within  $8^\circ$  of a  $1260^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity.

For an Al substrate with deep UV photolithography, at  $m=9$  an appropriate refractive index  $n$  value is about 2.3, an appropriate absorptive index  $k$  value is about 0.10 and an appropriate thickness value is about 2430 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $1620^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity. For a W-Si substrate with deep UV photolithography, at  $m=9$  an appropriate refractive index  $n$  is about 2.3, an absorptive index  $k$  is about 0.10 and a thickness about 2430 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $1620^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity.

For an Al substrate with deep UV photolithography, at  $m=11$  an appropriate refractive index  $n$  value is about 2.3, an appropriate absorptive index  $k$  value is about 0.081 and an appropriate thickness value is about 2965 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of  $1980^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity. For a W-Si substrate with deep UV photolithography, at  $m=11$  an appropriate refractive index  $n$  is about 2.3, a absorptive index  $k$  is about 0.081 and thickness about 2965 angstroms. These solutions satisfy Equation 1 to within  $8^\circ$  of a  $2965^\circ$  phase difference. For Equation 2, the difference in intensity between ray 3 and ray 5 is about 5 percent of incident intensity.

Solutions for higher odd multiples such as  $m=13$ , 15, or 17 can be found with increases in ARL thickness that satisfy Equation 1. With this increased thickness, a suitable absorptive index  $k$  value can be determined to satisfy Equation 2.

Appendix B includes a characterization chart showing some of the preferred ranges of the various process parameters used, and the effects of silane, ammonia, and temperature on the chemical and optical properties of the ARL deposited. The graphs following the chart show how reflectance versus wavelength, refractive index  $n$  versus wavelength, and absorptive index  $k$  versus wavelength vary with  $\text{SiH}_4$  flow, supporter and

manifold spacing, chamber pressure, RF power, N<sub>2</sub>O flow, N<sub>2</sub> flow, He flow, total gas flow, and temperature varies from a center value. The center values for SiH<sub>4</sub> flow, supporter and manifold spacing, chamber pressure, RF power, N<sub>2</sub>O flow, N<sub>2</sub> flow, He flow and temperature are respectively 51 sccm, 500 mils, 4.6 torr, 160 W, 30 sccm, 200 sccm, 2000 sccm, and 350°C. These values also correspond to the values of the respective process parameters that were kept constant as one of these process parameters is varied.

Appendix C includes graphs which show the effect of chamber pressure, RF power, supporter and manifold spacing, SiH<sub>4</sub> flow, N<sub>2</sub>O flow, He flow, and N<sub>2</sub> flow on the deposition rate of the process for depositing an ARL, and on the refractive index n, absorptive index k, and uniformity of the ARL deposited. In these graphs, if not stated otherwise, silane flow, supporter and manifold spacing, chamber pressure, RF power, N<sub>2</sub>O flow, N<sub>2</sub> flow, and helium flow are all kept constant at 51 sccm, 500 mils, 4.6 torr, 160 W, 30 sccm, 200 sccm, and 2000 sccm, respectively.

As will be understood by those of skill in the art, the present invention could be embodied in other specific forms without departing from the spirit or essential characteristics thereof. Accordingly, the foregoing description is intended to be illustrative of the preferred embodiments, and reference should be made to the appended claims for setting forth the scope of the invention.

What is claimed is:

- 1        1. A substrate processing system, comprising:  
2            a vacuum chamber;  
3            a substrate supporter, located within the vacuum chamber, for holding a  
4            substrate;  
5            a gas manifold for introducing process gases into the chamber;  
6            a gas distribution system, coupled to the gas manifold, for distributing the  
7            process gases to the gas manifold from gas sources;  
8            a power supply coupled between the substrate supporter and the gas  
9            manifold;  
10           a vacuum system for controlling pressure within the vacuum chamber;  
11           a controller, including a computer, for controlling the gas distribution  
12           system, the power supply and the vacuum system; and  
13           a memory coupled to the controller comprising a computer usable medium  
14           having a computer readable program code embodied therein for directing operation of the  
15           substrate processing system, the computer readable program code including:  
16                computer readable program code for causing the gas distribution  
17                system to introduce a first process gas comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into  
18                the chamber to deposit a first plasma enhanced CVD layer over the wafer; and  
19                computer readable program code for causing the gas distribution  
20                system to introduce a second process gas comprising He into the chamber to  
21                control the deposition rate of the first layer.
  
- 1        2. A substrate processing system as in claim 1 wherein the computer  
2           readable program code for causing the gas distribution system to introduce the first  
3           process gas comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into the chamber controls the  
4           introduction of the SiH<sub>4</sub> to be between 5 to 300 sccm, and the rate of N<sub>2</sub>O to be between  
5           5 to 300 sccm.

1           3. A substrate processing system as in claim 2 wherein the computer  
2 readable program code for causing the gas distribution system to introduce a second  
3 process gas comprising He into the chamber controls the chamber pressure at about 1 to  
4 6 torr.

1           4. A substrate processing system as in claim 3 wherein the computer  
2 readable program code for causing the gas distribution system to introduce the first  
3 process gas comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into the chamber controls the  
4 introduction of the SiH<sub>4</sub> to be at a ratio of between 0.5 to 3 times the amount of N<sub>2</sub>O.

1           5. A substrate processing system as in claim 1 further comprising:  
2           computer readable program code for causing the gas distribution system to  
3 introduce a third process gas comprising NH<sub>3</sub> into the chamber; and  
4           computer readable program code for causing the gas distribution system to  
5 introduce a fourth process gas comprising N<sub>2</sub> into the chamber.

1           6. A substrate processing system as in claim 5 wherein:  
2           the computer readable program code for causing the gas distribution system  
3 to introduce a third process gas comprising NH<sub>3</sub> into the chamber controls the introduction  
4 of the NH<sub>3</sub> to be between a rate of 0 to 300 sccm; and  
5           the computer readable program code for causing the gas distribution system  
6 to introduce a fourth process gas comprising N<sub>2</sub> into the chamber controls the introduction  
7 of the N<sub>2</sub> to be between a rate of 0 to 4000 sccm.

1           7. A substrate processing system as in claim 1 further comprising  
2 computer readable program code for controlling the gas distribution system to operate for  
3 a specified time period.

1           8. A substrate processing system as in claim 7 wherein the computer  
2 readable program code for controlling the gas distribution system to operate for a specified  
3 time period comprises computer readable program code for causing the first plasma

4 enhanced CVD layer to be formed to a thickness which is an odd multiple, greater than  
5 one, of a wavelength of light to be used in a subsequent process operation on the layer.

1                   9. A substrate processing system as in claim 2 wherein the computer  
2 readable program code for causing the gas distribution system to introduce the first  
3 process gas comprising a mixture of SiH<sub>4</sub> and N<sub>2</sub>O into the chamber controls the  
4 introduction of the SiH<sub>4</sub> to be between 15 to 160 sccm, and the rate of N<sub>2</sub>O to be between  
5 a rate of 15 to 160 sccm.

1                   10. A substrate processing system as in claim 9 further comprising:  
2                   computer readable program code for causing the gas distribution system to  
3 introduce a third process gas comprising NH<sub>3</sub> into the chamber at a rate of less than 150  
4 sccm; and

computer readable program code for causing the gas distribution system to introduce a fourth process gas comprising N<sub>2</sub> into the chamber at a rate of less than 300 sccm.

1 11. A method for achieving plasma stability for a process during  
2 deposition of an antireflective layer in a processing chamber, the method comprising the  
3 steps of:

4 adding an amount of inert gas to the process to achieve a desired chamber  
5 pressure; and

6 controlling the amount of inert gas added to the process to control the  
7 processing chamber pressure.

determining an amount of an inert gas required to achieve a desired lower deposition rate;

- adding the inert gas to the process; and
- controlling the step of adding the inert gas to achieve the desired lower deposition rate.

14. The method of claim 13 wherein the inert gas comprises helium.

15. The method of claim 13 wherein the process comprises a plasma enhanced silane oxide process.

16. The method of claim 13 wherein the process comprises a plasma enhanced silane oxynitride process.

17. The method of claim 16 wherein the process comprises a plasma enhanced silane nitride process.

18. The method of claim 13 further comprising the steps of:  
maintaining a chamber pressure in the range of 1-6 Torr;  
connecting the chamber to a RF power supply;  
supporting the substrate on a supporter;  
heating the substrate;  
introducing SiH<sub>4</sub> into the chamber at a rate of 5-300 sccm;  
introducing N<sub>2</sub>O into the chamber at a rate of 5-300 sccm;

19. The method of claim 18 wherein the step of heating comprises heating the substrate to a temperature in the range of 200-400°C.

20. The method of claim 18 wherein the step of supporting the substrate comprises placing the supporter at a distance from a gas distribution system in the range of 200-600 mils.

1           21. The method of claim 13 further comprising:  
2           introducing NH<sub>3</sub> into the chamber at a rate of less than 300 sccm; and  
3           introducing N<sub>2</sub> into the chamber at a rate of less than 4000 sccm.

1           22. The method of claim 13 further comprising the steps of:  
2           forming an antireflective layer over a substrate;  
3           forming a layer of additional material on the antireflective layer; and  
4           wherein the antireflective layer comprises a film which, at a given exposure  
5           wavelength, will have a first reflection from an first interface between the additional  
6           material and the antireflective layer, and a second reflection from a second interface  
7           between the antireflective layer and the substrate, the second reflection being an odd  
8           number which is at least 3 multiplied by 180° out of phase with the first reflection,  
9           whereby the first and second reflections at least partially cancel each other.

1           23. The method of claim 22 wherein the additional material comprises  
2           photoresist.

1           24. The method of claim 23 wherein the antireflective layer has a  
2           refractive index in the range of 2.1-2.4, and an absorptive index in the range of 0.2-0.5.

1           25. The method of claim 24 wherein the antireflective layer has a  
2           thickness between 500-1000 angstroms.

1           26. A process for depositing an antireflective layer on a substrate in a  
2           semiconductor processing chamber comprising the steps of:  
3           using He to provide a chamber pressure in the range of 1-6 Torr;  
4           connecting the chamber to a RF power supply to receive 50 to 500 Watts;  
5           supporting the substrate within the chamber;  
6           heating the substrate to a temperature in the range of 200-400 °C;  
7           introducing SiH<sub>4</sub> through a gas distribution system at a 5-300 sccm; and

introducing  $\text{N}_2\text{O}$  through the gas distribution system at a rate of 5-300 sccm.

27. The process of claim 26 further comprising the step of introducing  $\text{NH}_3$  into the chamber at a rate of 0-300 sccm.

28. The process of claim 27 further comprising the step of introducing N<sub>2</sub> into the chamber at a rate of 0-4000 sccm.

29. The process of claim 26 wherein the He is introduced into the chamber at a rate of 5-5000 sccm.

30. The process of claim 26, wherein the antireflective layer has a refractive index  $n$ , an absorptive index  $k$ , a thickness  $t$ , and a reflectance  $r$ , the process further comprising the step of increasing the rate at which the  $\text{NH}_3$  is introduced into the chamber to cause the refractive index  $n$  and the thickness  $t$  to increase, and the absorptive index  $k$  and the reflectance  $r$  to decrease.

31. The process of claim 26, wherein the antireflective layer has a refractive index  $n$ , an absorptive index  $k$ , a thickness  $t$ , and a reflectance  $r$ , the process further comprising the step of increasing the rate at which the  $N_2$  is introduced into the chamber to cause the refractive index  $n$ , the absorptive index  $k$ , and the reflectance  $r$  to decrease, and the thickness  $t$  to increase.

32. The process of claim 26, wherein the antireflective layer has a refractive index  $n$ , an absorptive index  $k$ , a thickness  $t$ , and a reflectance  $r$ , the process further comprising the step of increasing the rate at which the He is introduced into the chamber to cause the refractive index  $n$ , the absorptive index  $k$ , and the reflectance  $r$  to increase, and the thickness  $t$  to decrease.

1                   33. The process of claim 26, wherein the antireflective layer has a  
2 refractive index n, an absorptive index k, a thickness t, and a reflectance r, the process  
3 further comprising the step of increasing the temperature to cause the refractive index n,  
4 the absorptive index k, the thickness t, and the reflectance r to increase.

1                   34. The process of claim 26, wherein the antireflective layer has a  
2 refractive index n, an absorptive index k, a thickness t, and a reflectance r, the process  
3 further comprising the step of increasing the chamber pressure to cause the refractive  
4 index n, the absorptive index k, the thickness t, and the reflectance r to decrease.

1                   35. The process of claim 26, wherein the antireflective layer has a  
2 refractive index n, an absorptive index k, a thickness t, and a reflectance r, the process  
3 further comprising the step of increasing the power supplied to the chamber to cause the  
4 refractive index n, the absorptive index k, and the reflectance r to decrease, and the  
5 thickness t to increase.

1                   36. The process of claim 26, wherein the antireflective layer has a  
2 refractive index n, an absorptive index k, a thickness t, and a reflectance r, the process  
3 further comprising the step of increasing the distance between the supporter and the gas  
4 distribution system to cause the refractive index n, the absorptive index k, the thickness  
5 t, and the reflectance r to decrease.

1                   37. The process of claim 26, wherein the antireflective layer has a  
2 refractive index n, an absorptive index k, a thickness t, and a reflectance r, the process  
3 further comprising the step of increasing the rate at which SiH<sub>4</sub> is introduced into the  
4 chamber to cause the refractive index n, the absorptive index k, the thickness t, and the  
5 reflectance r to increase.

1                   38. The process of claim 26, wherein the antireflective layer has a  
2 refractive index n, an absorptive index k, a thickness t, and a reflectance r, the process  
3 further comprising the step of increasing the rate at which N<sub>2</sub>O is introduced into the

4 chamber to cause the refractive index  $n$ , the absorptive index  $k$ , and the reflectance  $r$  to  
5 decrease, and the thickness  $t$  to increase.

1 39. A method of forming a photoresist pattern, the method comprising  
2 the steps of:

3 forming an antireflective layer on a substrate; and

4 forming a layer of photoresist on the antireflective layer, the layer having  
5 a thickness and refractive indices such that a first reflection from an interface between the  
6 photoresist and the antireflective layer will be at least  $540^\circ$  out of phase with a second  
7 reflection from an interface between the antireflective layer and the substrate.

1 40. The method of claim 39 wherein:

2 the substrate comprises Al;

3 the first and second reflections are from light having a wavelength of about  
4 248 nm; and

5 wherein the antireflective film has a refractive index  $n$  in the range of  
6 2.1-2.4, an absorptive index  $k$  in the range of 0.2-0.5, and a thickness  $t$  in the range of  
7 500-1000 angstroms.

1 41. The method of claim 40 wherein the substrate further comprises at  
2 least one of Si and Cu.

1 42. The method of claim 39 wherein light is used to expose the  
2 photoresist, and the light has a wavelength in the range of 190-900 nm.

1 43. An improved antireflective layer for use in the manufacture of  
2 semiconductor devices comprising:

3 a layer comprising SiON having a refractive index  $n$  in the range of  
4 1.7-2.9, an absorptive index  $k$  in the range of 0-1.3, and a thickness in the range of  
5 200-3000 angstroms; and

6 wherein at an exposure wavelength of 365 nm or less, a phase shift of an  
7 odd multiple of at least 3 multiplied by 180° exists between a first reflection from an  
8 interface between an overlying layer of photoresist and the antireflective layer and a  
9 second reflection from an interface between the antireflective layer and a substrate, the  
10 first reflection having almost the same intensity as the second reflection to thereby  
11 substantially cancel the first and the second reflections.

the *Journal of the Royal Society of Medicine* (1956, 49, 101-102) and the *Journal of Clinical Pathology* (1956, 10, 211-212).

**Method and Apparatus for Depositing Antireflective Coating****ABSTRACT OF THE DISCLOSURE**

This invention provides a stable process for depositing an antireflective layer. Helium gas is used to lower the deposition rate of plasma-enhanced silane oxide, silane oxynitride, and silane nitride processes. Helium is also used to stabilize the process, so that different films can be deposited. The invention also provides conditions under which process parameters can be controlled to produce antireflective layers with varying optimum refractive index, absorptive index, and thickness for obtaining the desired optical behavior.

5  
RCC02101:rcc/mks

RCC02101:rcc/mks

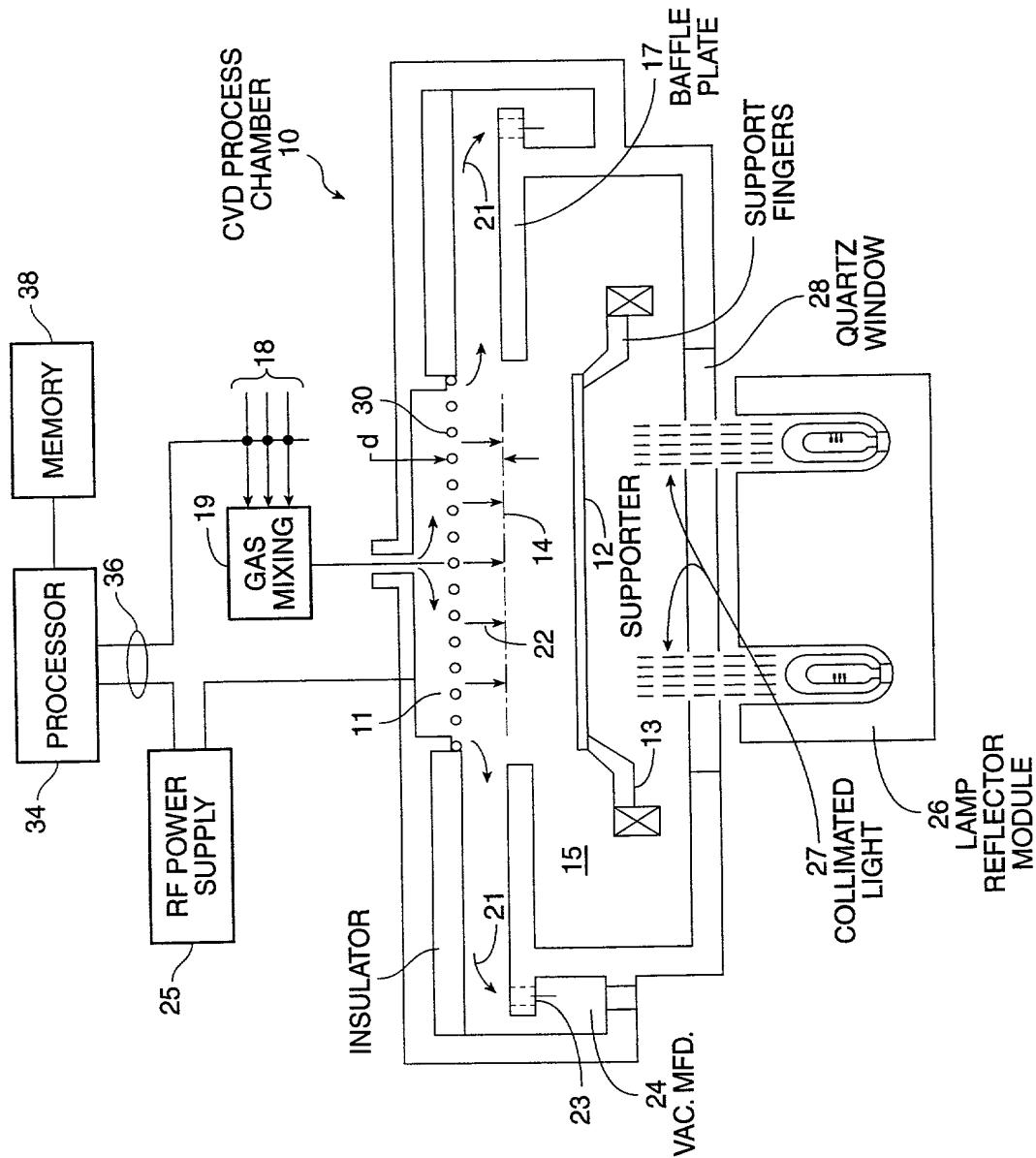


FIG. 1

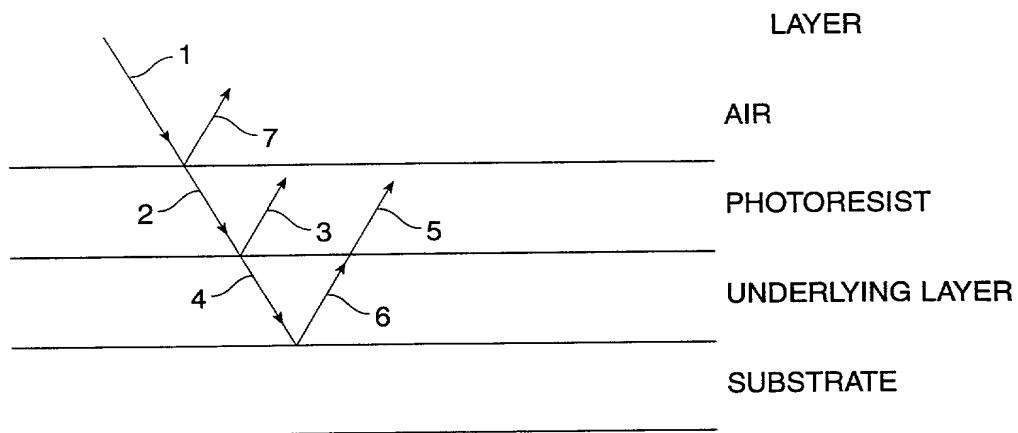


FIG. 2

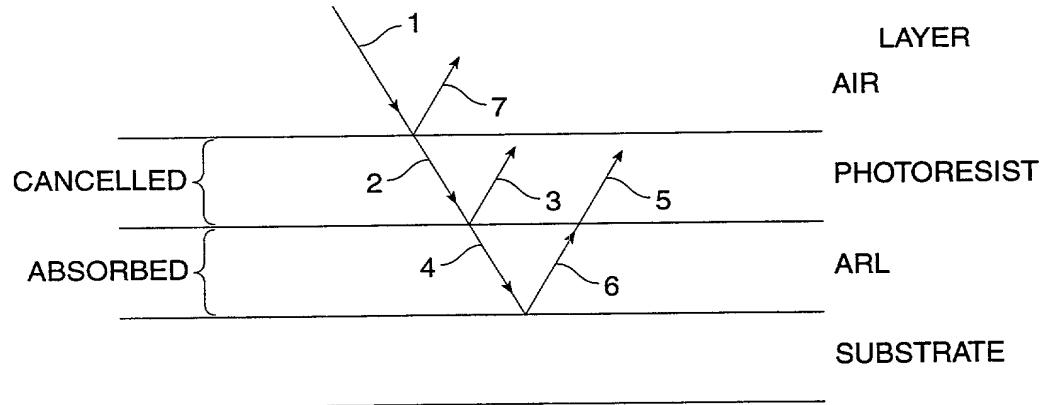


FIG. 3

INCREASE IN:	EFFECT OF INCREASE ON			
	n	k	t	r
TEMPERATURE	↑	↑	↑	↑
PRESSURE	↓	↓	↓	↓
POWER	↓	↓	↑	↓
SPACING	↓	↓	↓	↓
SiH <sub>4</sub>	↑	↑	↑	↑
N <sub>2</sub> O	↓	↓	↑	↓
NH <sub>3</sub>	↑	↓	↑	↓
N <sub>2</sub>	↓	↓	↑	↓
He	↑	↑	↓	↑
TOTAL GAS FLOW	↑	↑	↑	↑

FIG. 4

## COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

This declaration is of the following type:

- original
- divisional
- continuation
- continuation-in-part

### INVENTORSHIP IDENTIFICATION

My residence, post office address and citizenship are as stated below next to my name. I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

### TITLE OF INVENTION

*"Method and Apparatus for Depositing Antireflective Coating"*

### SPECIFICATION IDENTIFICATION

The specification of which:

- is attached hereto
- was filed on \_\_\_\_\_, under Serial No. (unknown), executed on even date herewith; or  
[X] Express Mail No. EM126756744US(as Serial No. not yet known)  
and was amended on \_\_\_\_\_ (if applicable)
- was described and claimed in PCT International Application No. \_\_\_\_\_  
filed on \_\_\_\_\_ and as amended under PCT Article 19 on \_\_\_\_\_

### ACKNOWLEDGEMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information I know to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56,

and which is material to the examination of this application; namely, information where there is a substantial likelihood that a reasonable Examiner would consider it important in deciding whether to allow the application to issue as a patent, and

- In compliance with this duty there is attached an Information Disclosure Statement in accordance with 37 CFR §1.98.

### PRIORITY CLAIM (35 U.S.C. §119)

I hereby claim foreign priority benefits under Title 35, United States Code, §119, of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below, and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed.

- No such applications have been filed.

Such applications have been filed as follows:

**A. Prior foreign/PCT application(s) filed within 12 mos. (6 mos. for design) prior to this application, and any priority claims under 35 U.S.C. §119**

<u>Country/PCT</u>	<u>Application No</u>	<u>Date Filed</u>	<u>Priority Claimed</u>
			<input type="checkbox"/> Yes <input type="checkbox"/> No
			<input type="checkbox"/> Yes <input type="checkbox"/> No
			<input type="checkbox"/> Yes <input type="checkbox"/> No

**B. All foreign application(s), if any, filed more than 12 mos. (6 mos for design) prior to this U.S. application**

Country:

Application No:

Filing date:

#### **PRIORITY CLAIM (35 U.S.C. §120)**

I hereby claim the benefit under Title 35, United States Code, §120, of any United States application(s) or PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information that is material to the examination of this application (namely, information where there is substantial likelihood that a reasonable Examiner would consider it important in deciding whether to allow the application to issue as a patent) which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application.

No such applications have been filed  
 Such applications have been filed, as follows:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Patented</u>	<u>Pending</u>	<u>Status</u>
08/567,338	December 5, 1995		X	Abandoned

#### **POWER OF ATTORNEY**

I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

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Peter J. Sgarbossa	Registration No. 25,610
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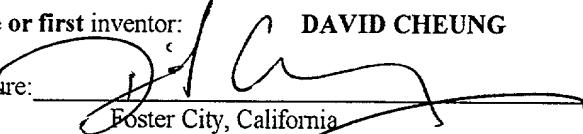
Direct telephone calls to:

Robert C. Colwell  
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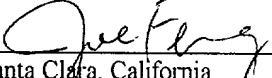
### DECLARATION

*I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and, further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Sec. 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.*

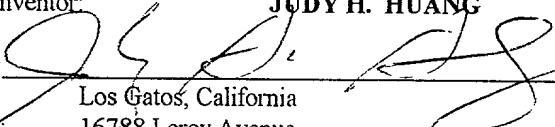
Full name of sole or first inventor: **DAVID CHEUNG**

Inventor's signature:  Date: 6/21/96  
Residence: Foster City, California  
Post Office Address: 235 Billingsgate Lane  
Foster City, California 94404  
U.S.A. Country of Citizenship: U.S.A.

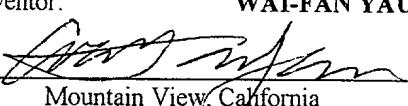
Full name of second inventor: **JOE FENG**

Inventor's signature:  Date: 6/21/96  
Residence: Santa Clara, California  
Post Office Address: 444 Saratoga Avenue, #711  
Santa Clara, California 95050  
U.S.A. Country of Citizenship: U.S.A.

Full name of third inventor: **JUDY H. HUANG**

Inventor's signature:  Date: 6-21-96  
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Los Gatos, California 95032  
U.S.A. Country of Citizenship: U.S.A.

Full name of fourth inventor: **WAI-FAN YAU**

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Residence: Mountain View, California  
Post Office Address: 1568 Gretel Lane  
Mountain View, California 94040  
U.S.A. Country of Citizenship: U.S.A.

(Declaration ends with this page)

"Express Mail" Label No. EM589214330US  
Date of Deposit October 15, 1999

PATENT

Attorney Docket No.: 16301-009320US  
Client Reference No.: AM1084D01/T9320

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Address" service under 37 CFR 1.10 on the date indicated above and is addressed to:

BOX PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

By: Diane Damonte

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

David Cheung, *et al.*

Application No.: Divisional application of  
S.N. 08/672,888

Filed: June 28, 1996

For: METHOD AND APPARATUS FOR  
DEPOSITING ANTIREFLECTIVE  
COATING

Examiner: Unassigned

Art Unit: Unassigned

**LETTER TO THE OFFICIAL  
DRAFTSPERSON**

BOX PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicants herein submit three (3) sheets of formal drawings to be made of record in the above-identified case.

Respectfully submitted,



Chun-Pok Leung  
Reg. No. 41,405

TOWNSEND and TOWNSEND and CREW LLP  
Two Embarcadero Center, 8<sup>th</sup> Floor  
San Francisco, California 94111-3834  
Tel: (415) 576-0200  
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RL:kad

## APPENDIX A

Let:

- $n_1$  = real index of refraction of air
- $n_2$  = real index of refraction of photoresist
- $n_3$  = real index of refraction of ARL
- $n_4$  = real index of refraction of substrate
- $k_1$  = imaginary index of refraction of air
- $k_2$  = imaginary index of refraction of photoresist
- $k_3$  = imaginary index of refraction of ARL
- $k_4$  = imaginary index of refraction of substrate
- $t$  = ARL thickness
- $m$  = odd integer
- $\lambda$  = wavelength of incident radiation
- $I_3$  = intensity of beam #3
- $I_5$  = intensity of beam #5
- $I_1$  = intensity of beam #1
- $T_{12}$  = transmittance from air to photoresist
- $R_{23}$  = reflectance from ARL in photoresist
- $a_2$  = absorption factor in photoresist
- $T_{23}$  = transmittance from photoresist to ARL
- $a_3$  = absorption factor in ARL
- $R_{34}$  = reflectance from substrate in ARL
- $\Delta\phi$  = change in phase angle

Using equations (1) and (2):

$$n_3 \cdot 2t = \frac{1}{2} (m\lambda) \quad m = 3 (540^\circ) \quad (1)$$

for  $|n_2| < |n_3| < |n_4|$

## APPENDIX A

$$I_3 = I_5 \quad (2)$$

$$I_3 = I_1 \cdot T_{12} \cdot R_{23} \cdot a_2$$

$$I_5 = I_1 \cdot T_{12} \cdot a_2 \cdot T_{23} \cdot a_3 \cdot R_{34} \cdot a_3 \cdot T_{23}$$

Therefore, because  $I_3 = I_5$

$$R_{23} = T_{23} \cdot a_3 \cdot R_{34} \cdot a_3 \cdot T_{23}$$

$$R_{23} = (T_{23})^2 \cdot (a_3)^2 \cdot R_{34}$$

These operations can be satisfied for  $n_2$ ,  $n_3$ ,  $n_4$ ,  $t$ ,  $\lambda$ ,  $k_2$ ,  $k_3$ , and  $k_4$  where

$$R_{23} = \frac{(n_2 - n_3)^2 + (k_2 - k_3)^2}{(n_2 + n_3)^2 + (k_2 + k_3)^2}$$

$$T_{23} = 1 - R_{23}$$

$$R_{34} = \frac{(n_3 - n_4)^2 + (k_3 - k_4)^2}{(n_3 + n_4)^2 + (k_3 + k_4)^2}$$

$$(a_3)^2 = \exp - 2 \frac{(2nk_3 \cdot 2t)}{\lambda}$$

For 248 nm with Al substrate,  $n_2 = 1.8$ ,  $k_2 = 0.011$

ARL  $n_3 = 2.3$ ,  $k_3 = 0.33$ ,  $\tau = 700 - 850 \text{ \AA}$

Al - Si  $n_4 = 0.089$ ,  $k_4 = 2.354$

$$R_{23} = 0.0197$$

$$(T_{23})^2 = 0.961$$

$$R_{34} = 0.714$$

$$(a_3)^2 = 0.0814$$

$$R_{23} = 0.0197$$

$$(T_{23})^2 \cdot (a_3)^2 R_{34} = 0.0559$$

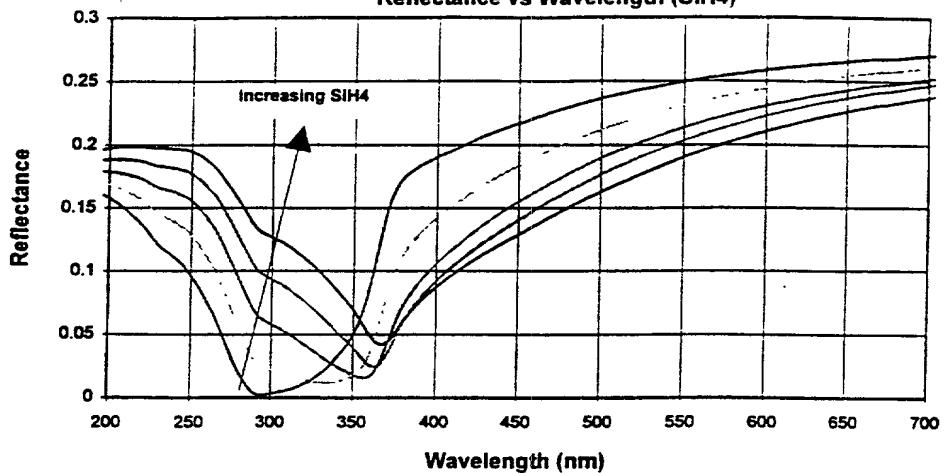
## APPENDIX B

### ARL Film RBS/HFS Sample Results

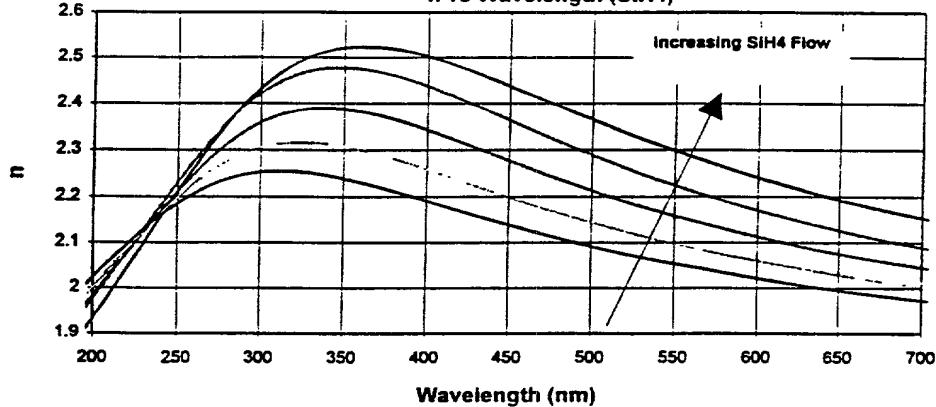
Process	DEEP-UV		DEEP-UV		DEEP-UV		I-LINE		I-LINE	
	[w/NH <sub>3</sub> ]	[w/o NH <sub>3</sub> ]	[+60% SiH <sup>4</sup> ]	[w/o NH <sub>3</sub> ]	[Standard]	[-60% SiH <sup>4</sup> ]	[300C dep]	[400C dep]	[w/o N <sub>2</sub> ]	
Faceplate	△ Nitride	△ Nitride	△ Nitride	△ Nitride	△ Nitride	△ Nitride	GG Nitride	GG Nitride	GG Nitride	GG Nitride
Pumping Plate	△ Nitride	△ Nitride	△ Nitride	△ Nitride	△ Nitride	△ Nitride	5-hole	5-hole	13-hole	13-hole
Blocker	Std. w/cntn holes	Std. w/cntn holes	Standard	Standard	Standard	Standard	Standard	Standard	Standard	Standard
Temperature(°C)	350	350	350	350	350	350	300	400	350	350
Pressure(torr)	4.6	4.6	4.6	4.6	4.6	4.6	5.3	5.3	5.0	5.0
Spacing(mils)	500	500	500	500	500	500	525	525	460	460
RF1 (watts)	160	160	160	160	160	160	130	130	150	150
SiH <sub>4</sub> (sccm)	51	51	81	51	51	20	63	63	82	82
N <sub>2</sub> O (sccm)	30	155	30	30	30	30	34	34	90	90
N <sub>2</sub> (sccm)	200	200	200	200	200	200	200	200	0	0
He (sccm)	2200	2200	2000	2000	2000	2000	1900	1900	2200	2200
NH <sub>3</sub> (sccm)	125	-	-	-	-	-	-	-	-	-
Thickness	700Å	1200Å	300Å	300Å	300Å	300Å	300Å	300Å	300	300
n	2.3	1.8	2.1	2.1	2.1	2.1	2.5	2.65	2.5	2.5
k	0.3	0.14	0.7	0.7	0.7	0.7	0.42	0.63	0.39	0.39
% reflectance	1.10%	1.80%	15%	15%	15%	15%	4.000%	9.00%	2.5%	2.5%
Atomic conc.(%)										
H	15.0%	13.5%	17.0%	16.0%	14.0%	20.0%	13.0%	13.0%	8.0%	8.0%
C	0.0%	0.0%	10.0%	5.0%	0.0%	8.0%	10.0%	10.0%	0.00%	0.00%
N	34.0%	15.0%	15.3%	18.1%	15.0%	17.0%	17.0%	17.0%	10.0%	10.0%
O	9.0%	34.5%	15.7%	15.9%	31.0%	15.0%	15.0%	15.0%	20.0%	20.0%
Si	42.0%	37.0%	42.0%	45.0%	40.0%	40.0%	45.0%	45.0%	52.0%	52.0%

## APPENDIX B

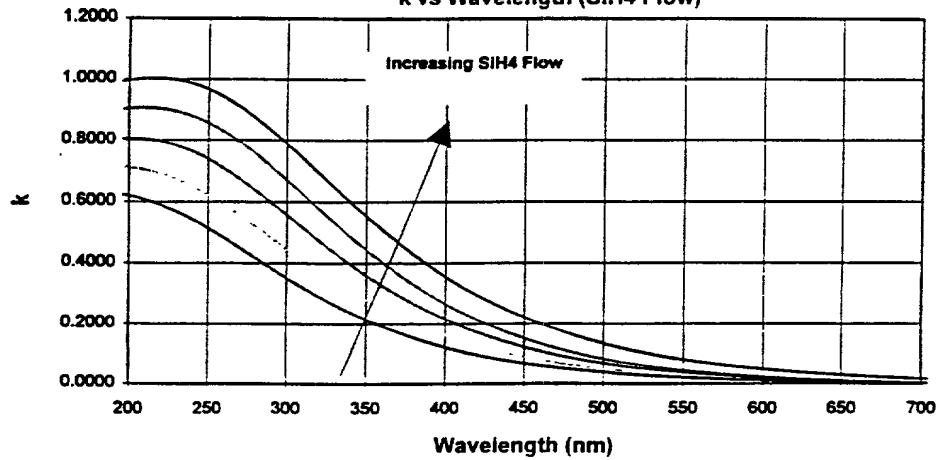
### ARC DEVELOPMENT Reflectance vs Wavelength (SiH4)



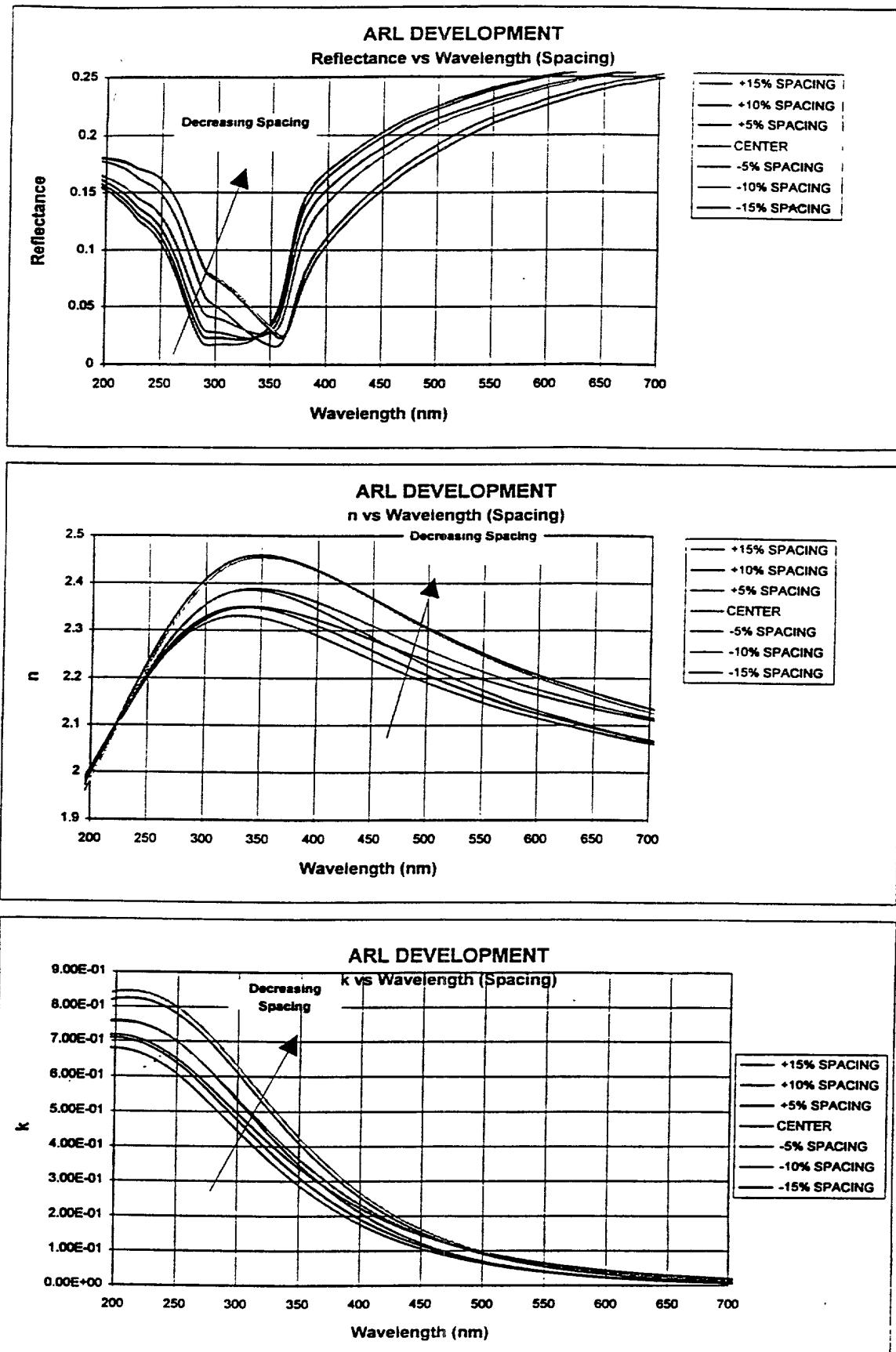
### ARC DEVELOPMENT $n$ vs Wavelength (SiH4)



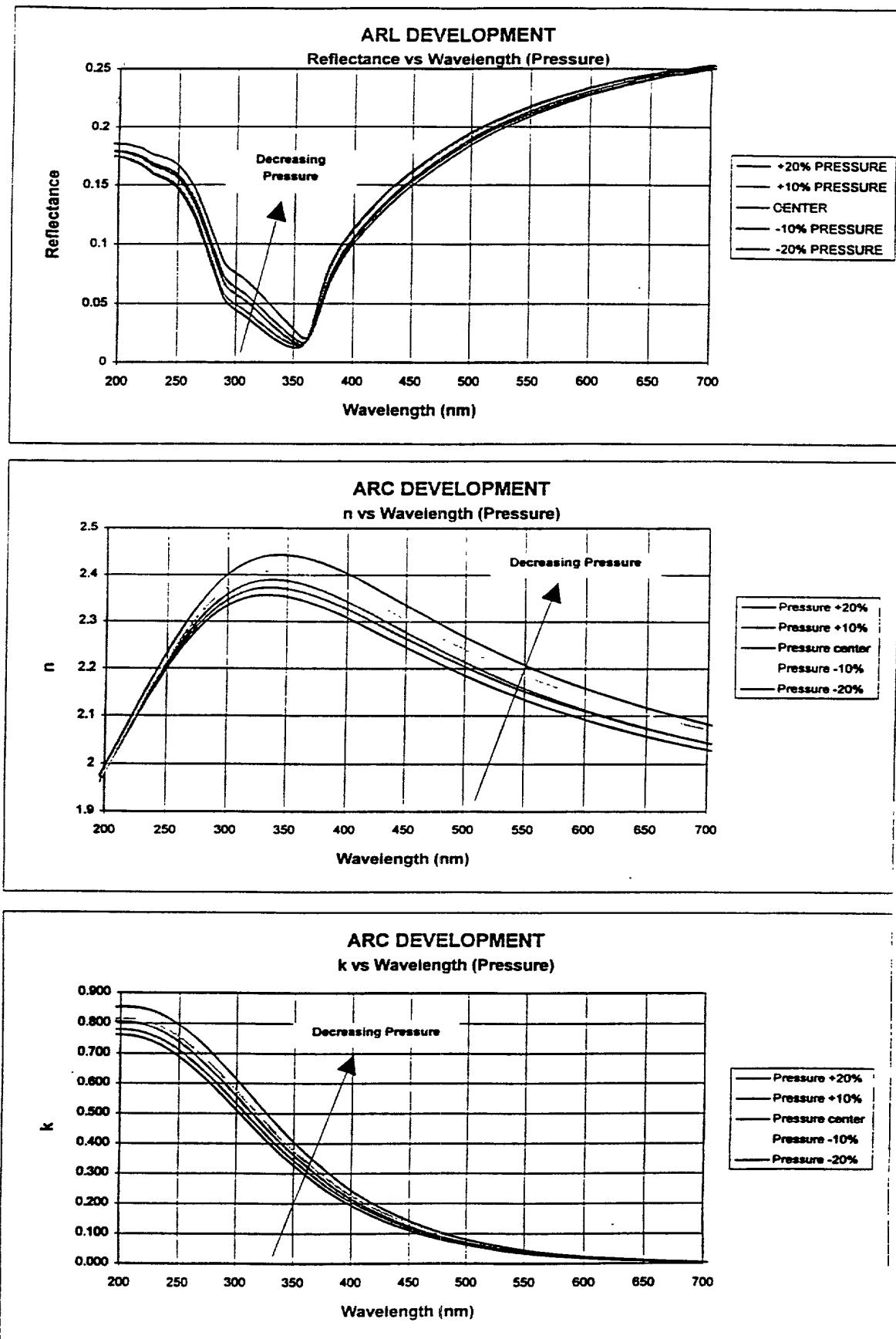
### ARC DEVELOPMENT $k$ vs Wavelength (SiH4 Flow)



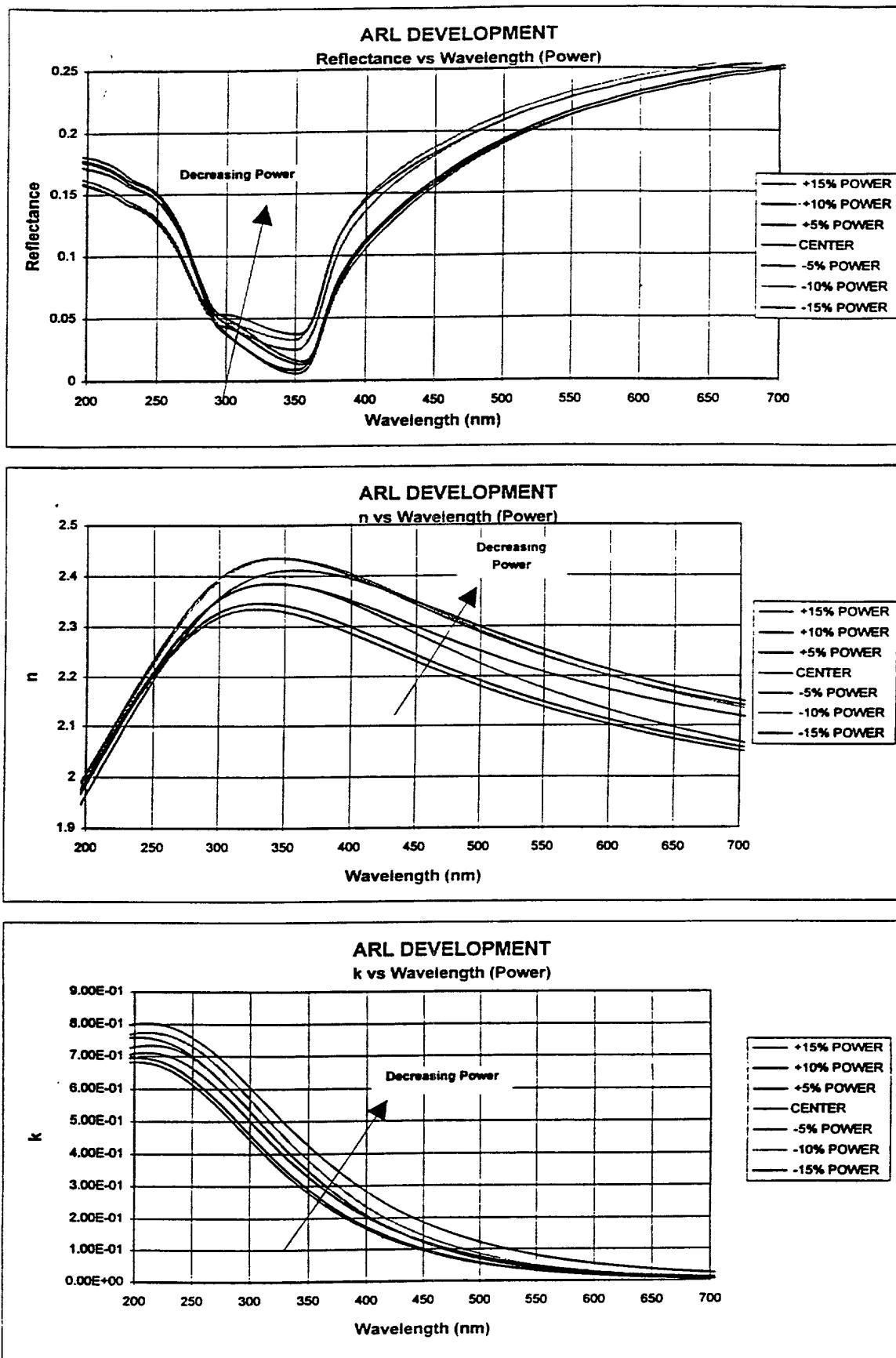
## APPENDIX B



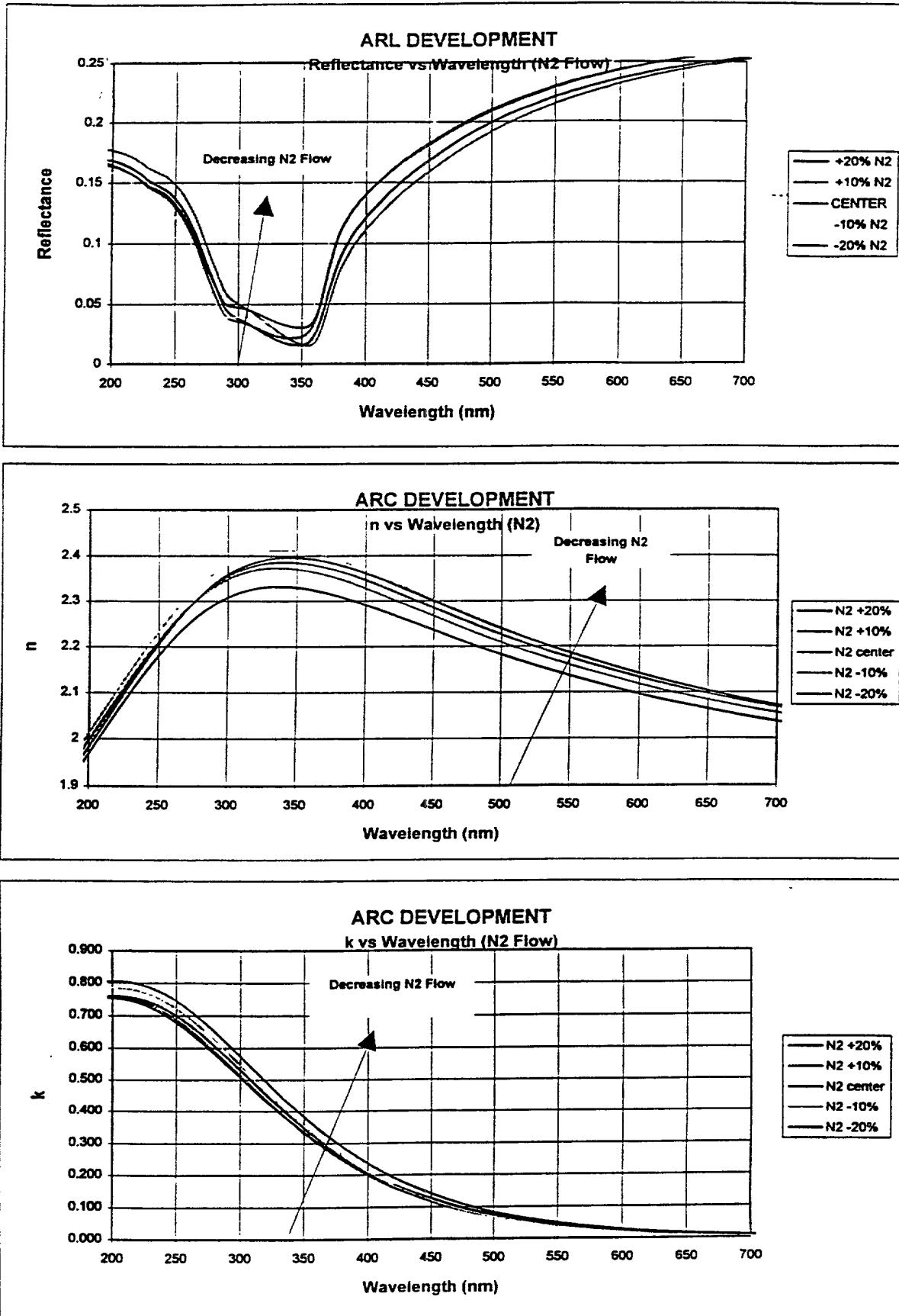
## APPENDIX B



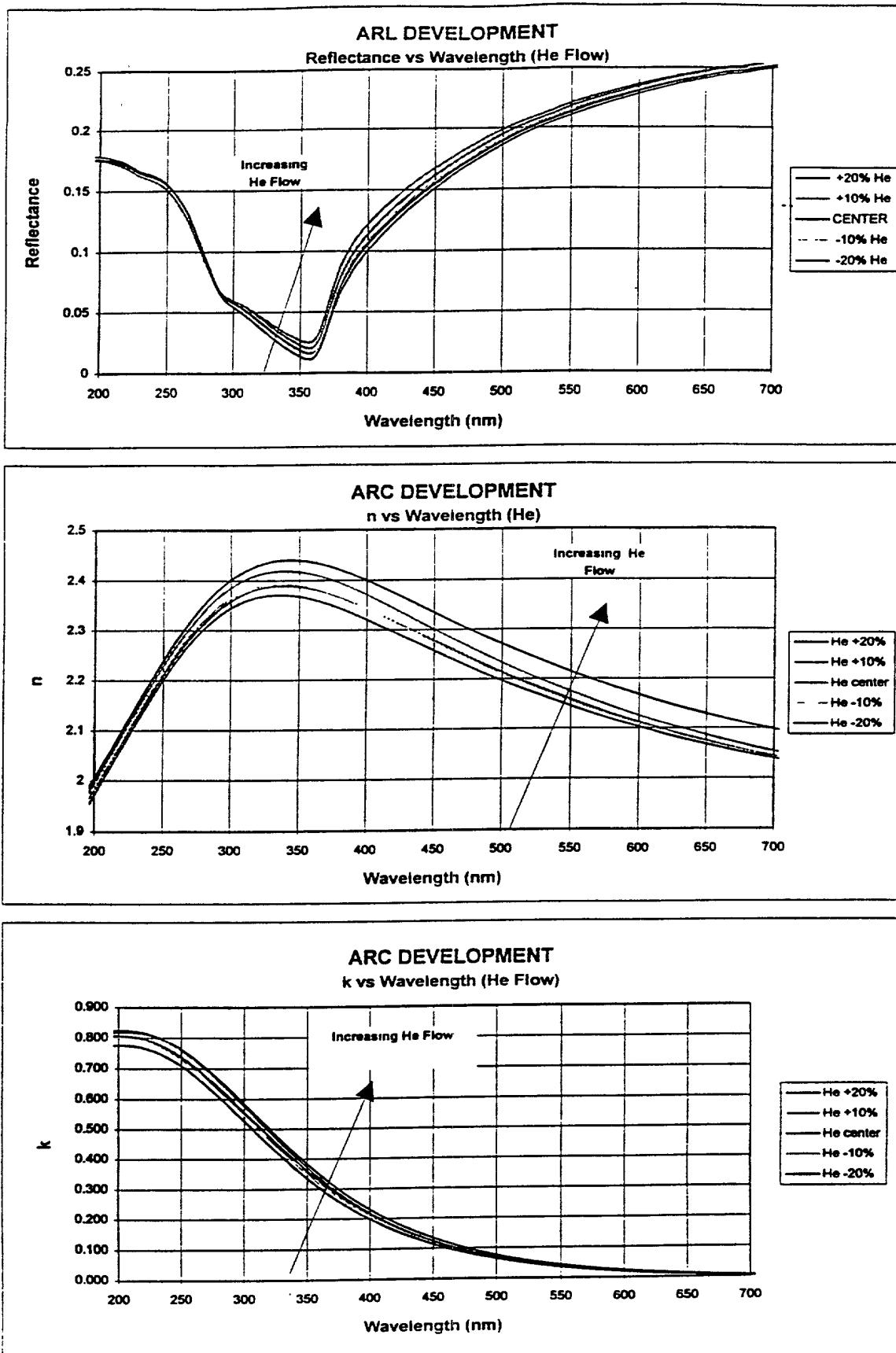
## APPENDIX B



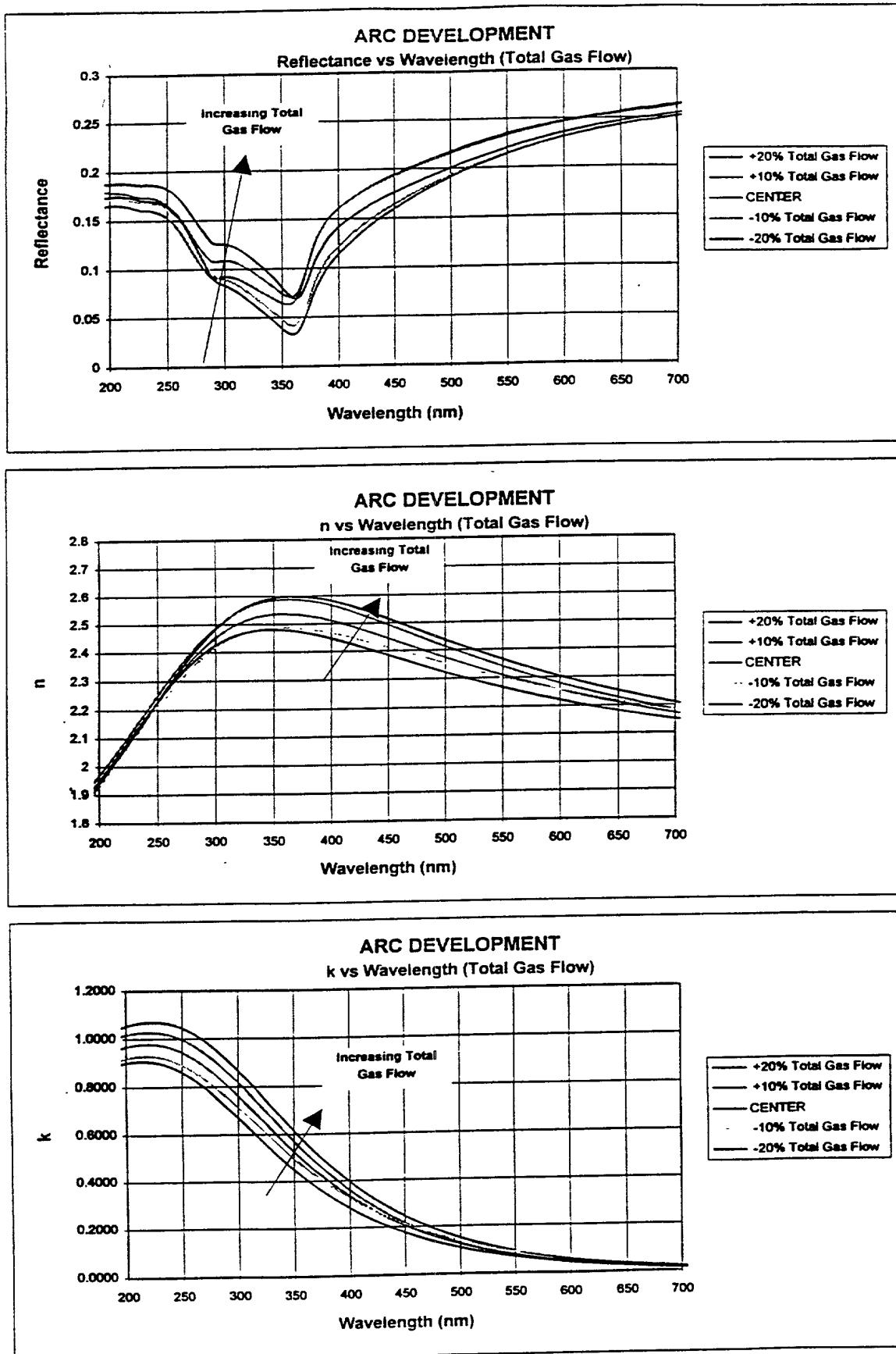
## APPENDIX B



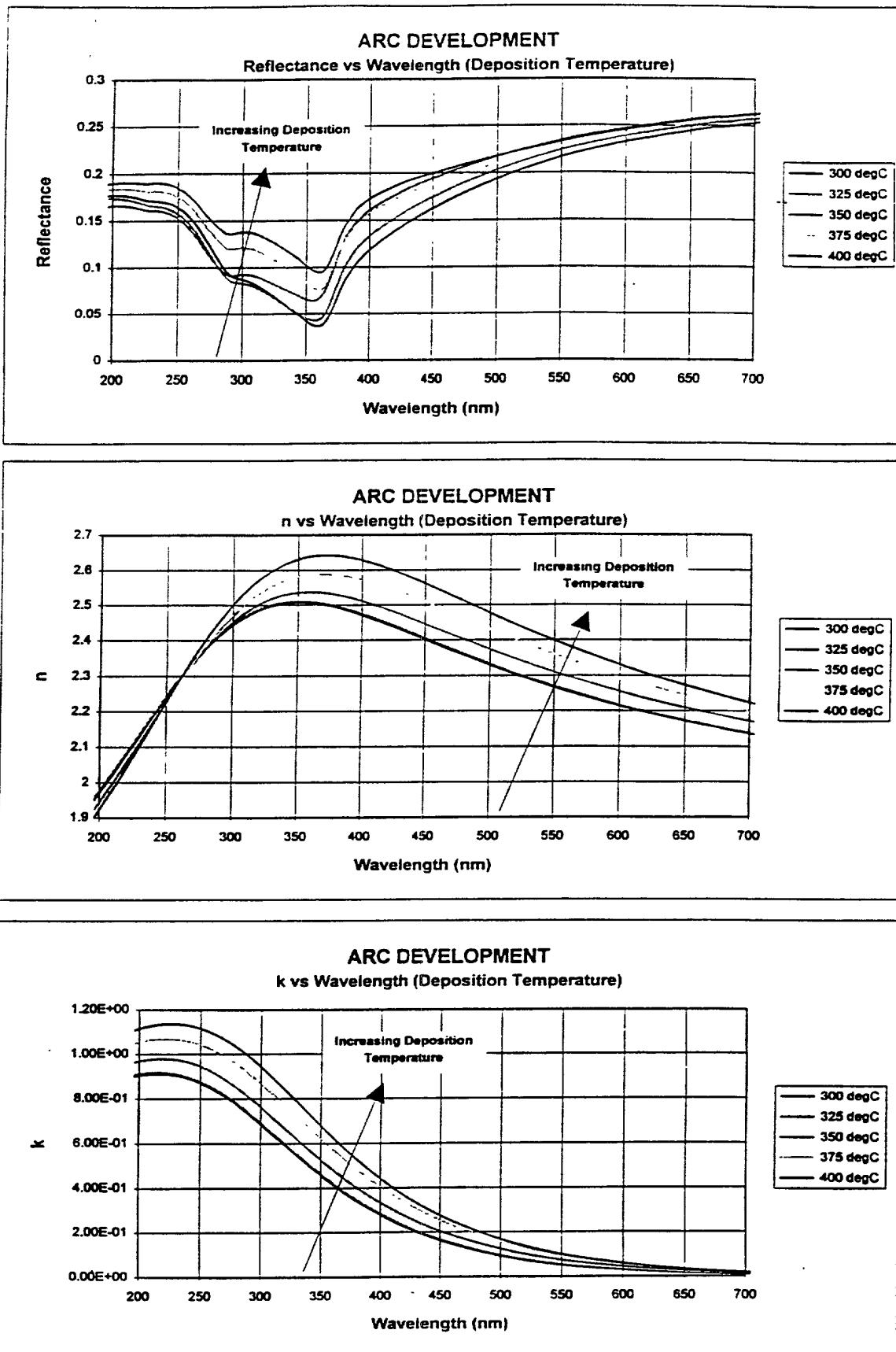
## APPENDIX B



## APPENDIX B

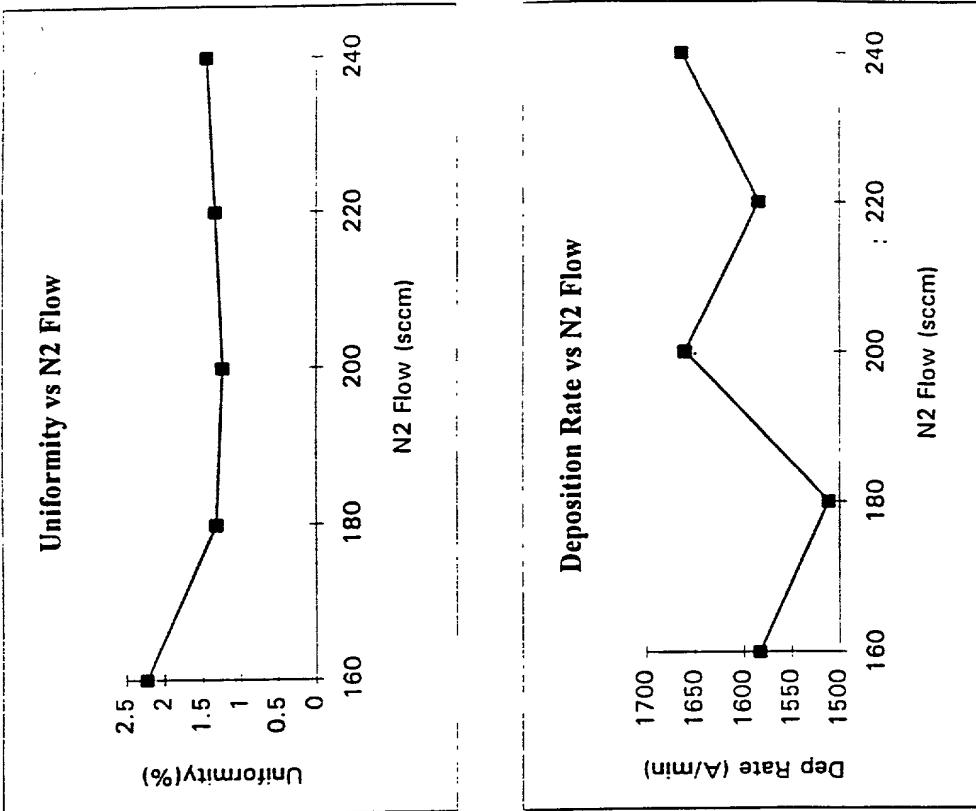
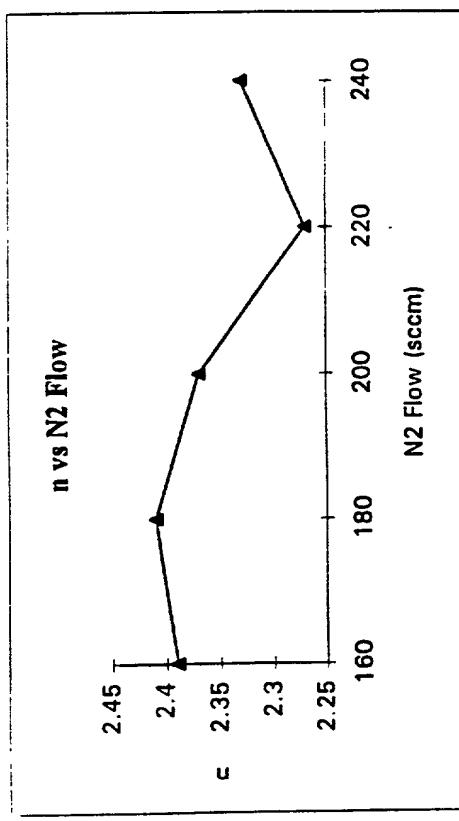
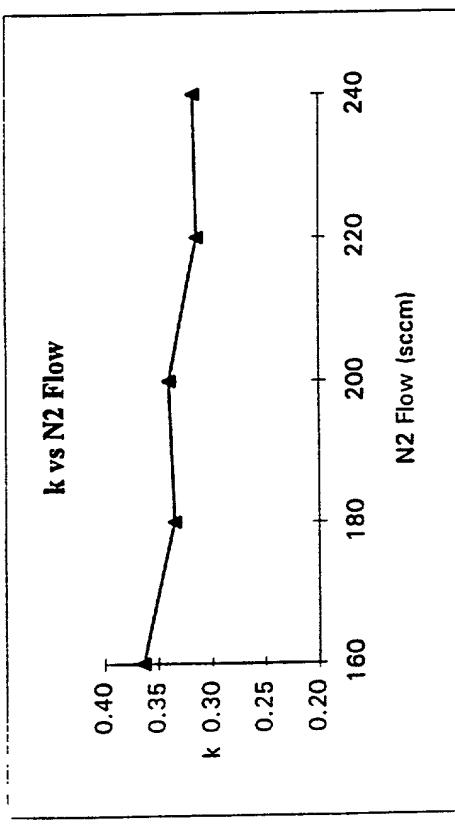


## APPENDIX B



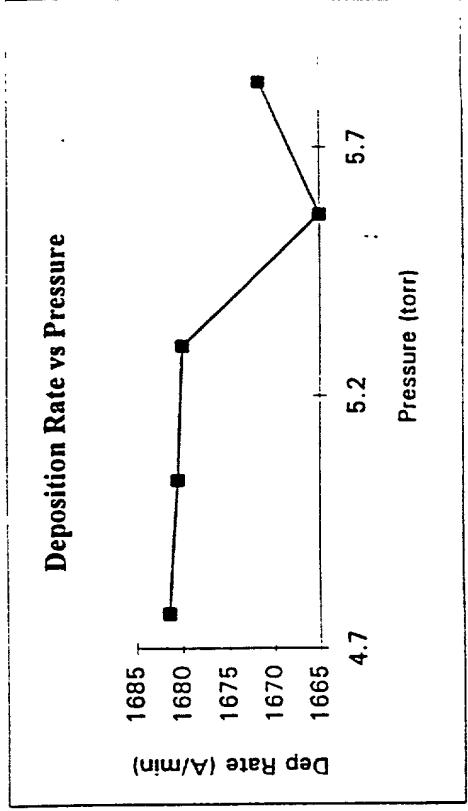
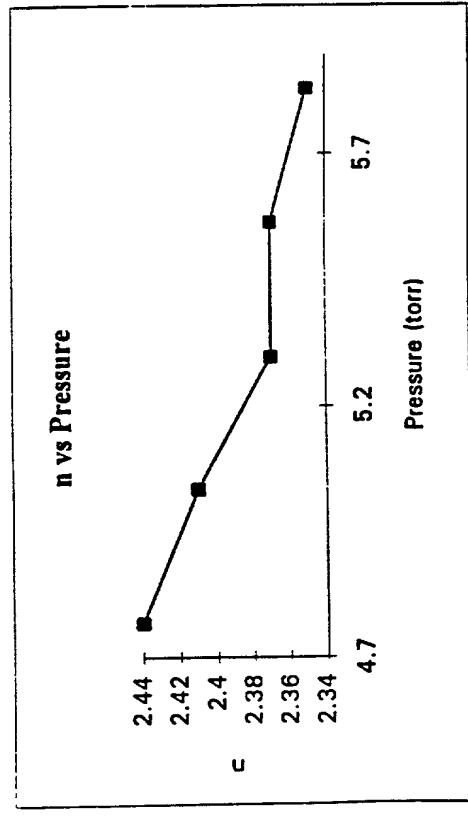
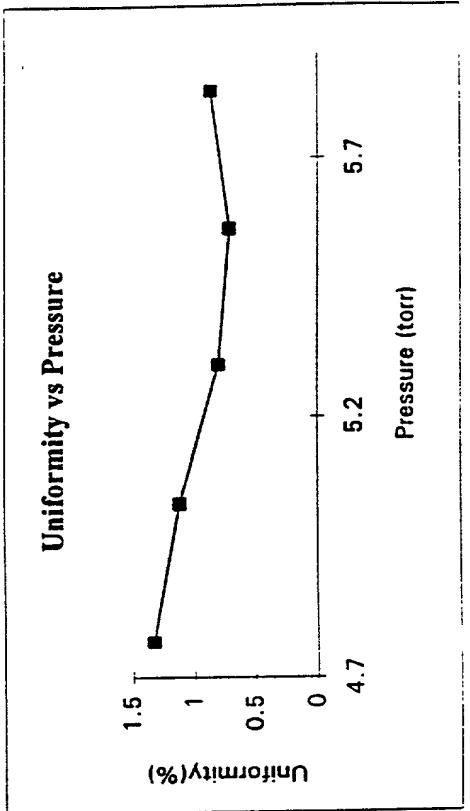
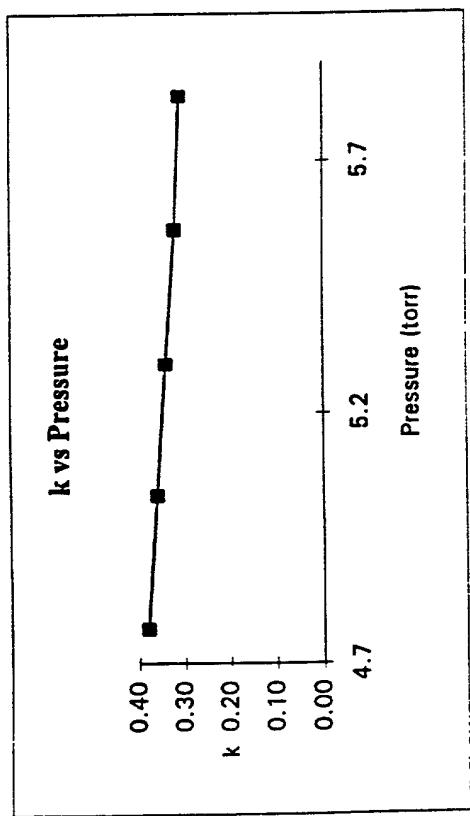
## APPENDIX C

### 200mm ARL Process Trends (300A/356nm)



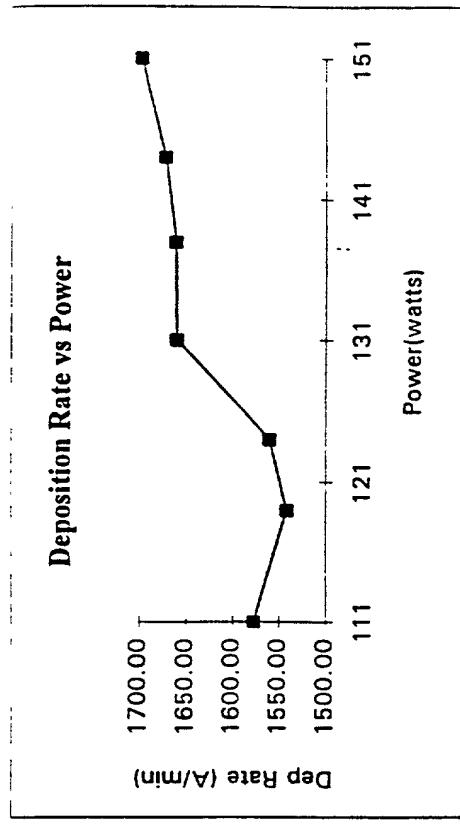
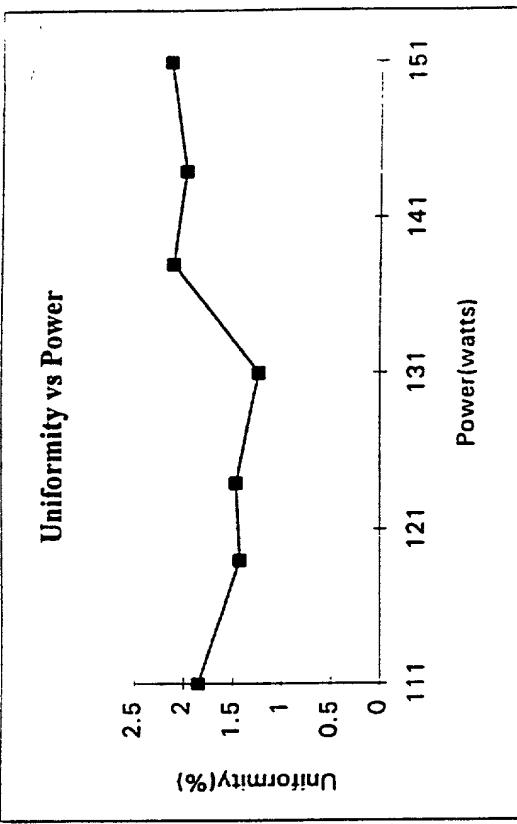
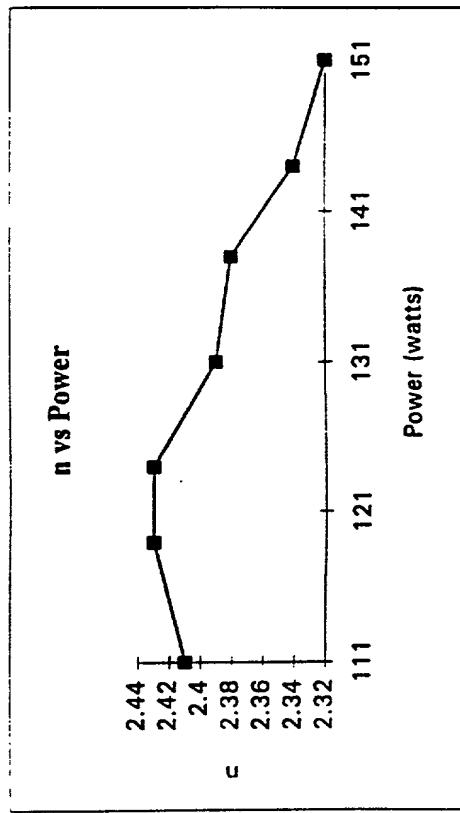
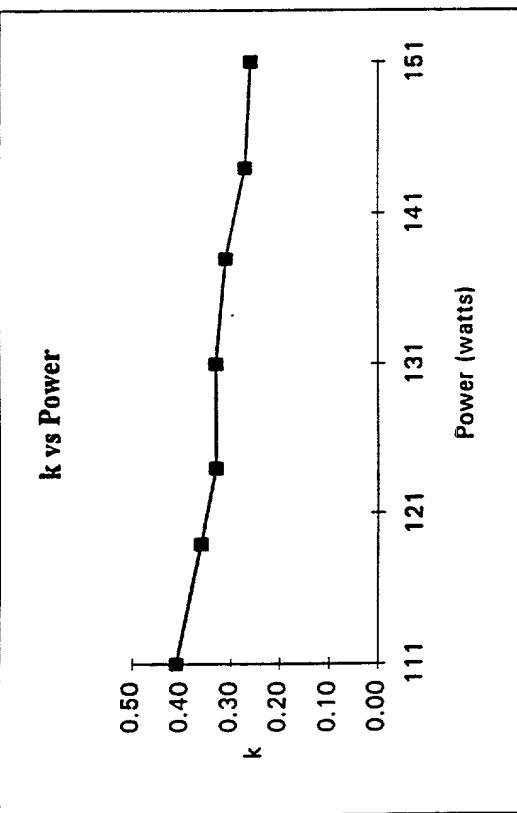
## 200mm ARL Process Trends (300A/356nm)

### APPENDIX C



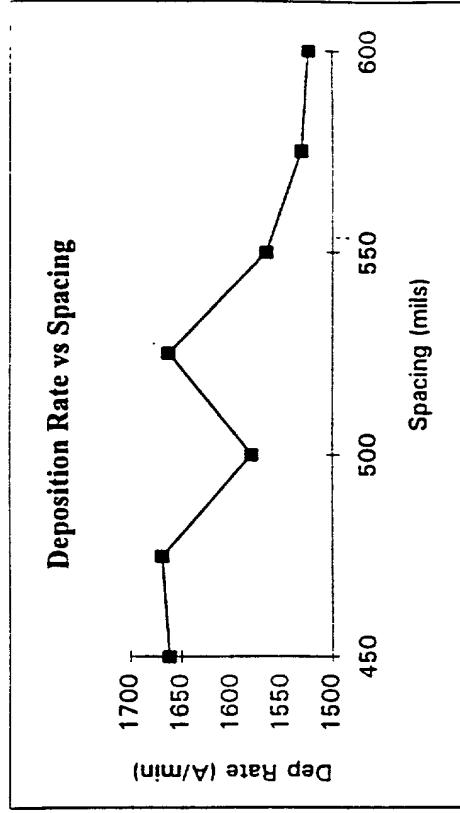
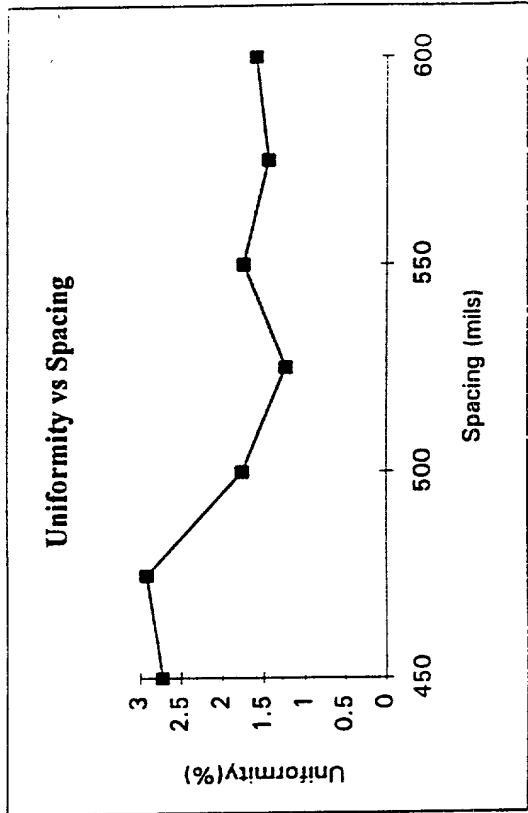
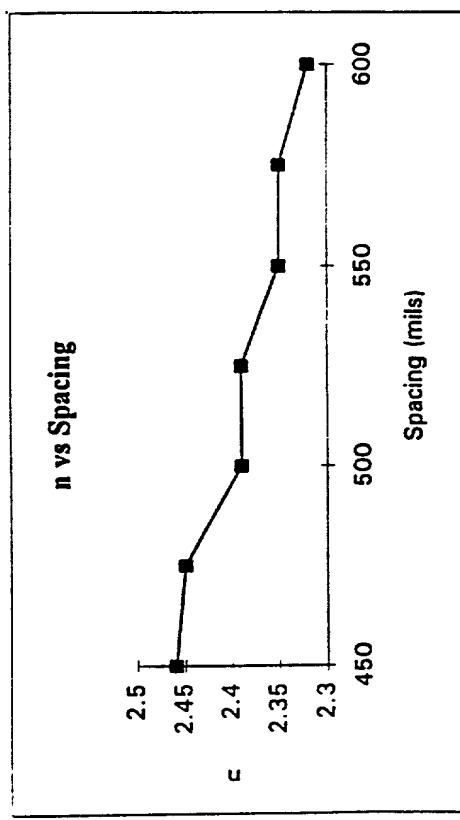
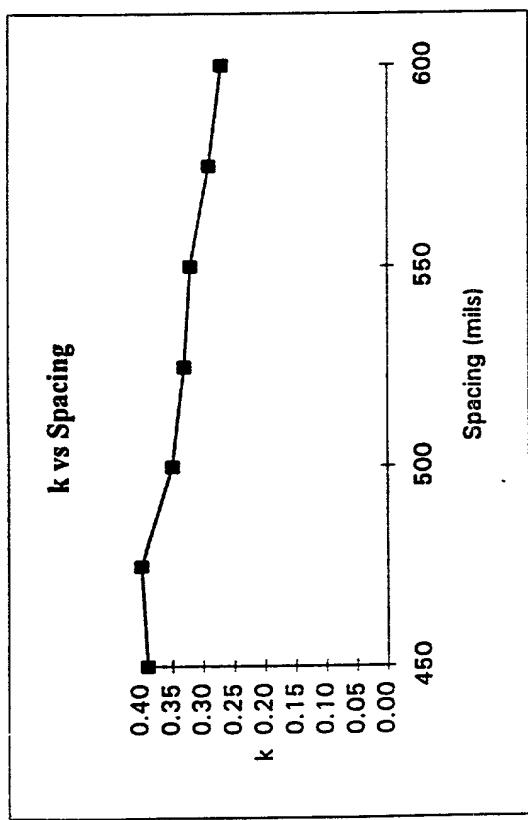
## APPENDIX C

### 200mm ARL Process Trends (300A/356nm)



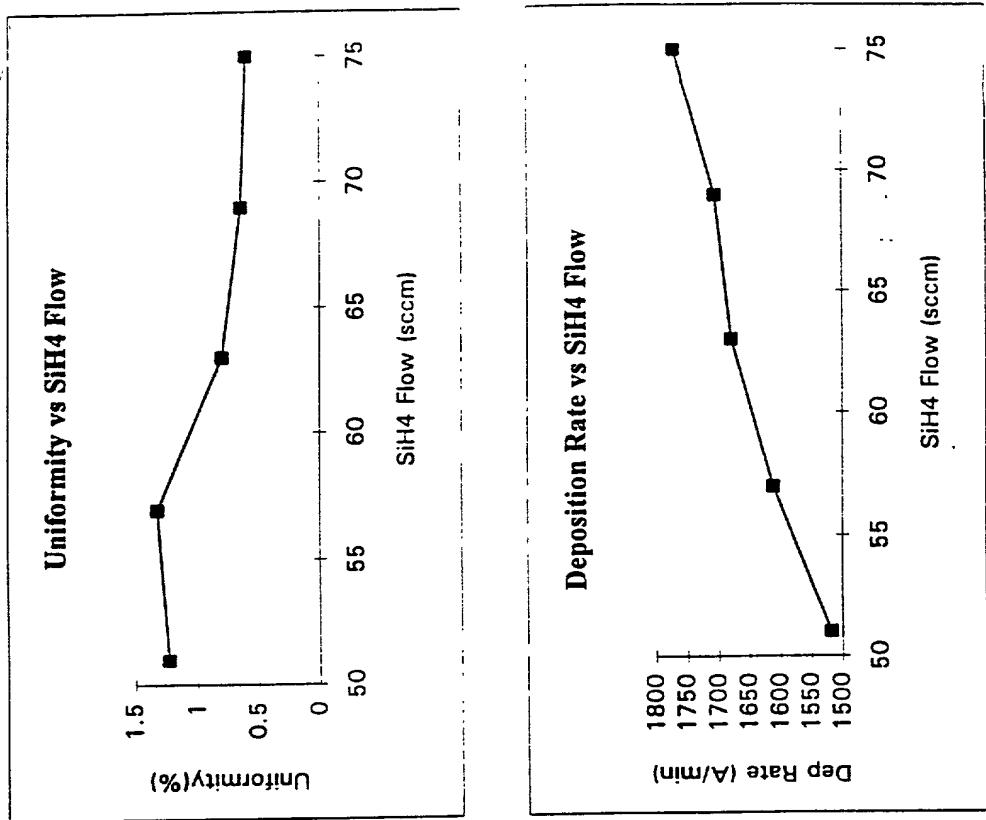
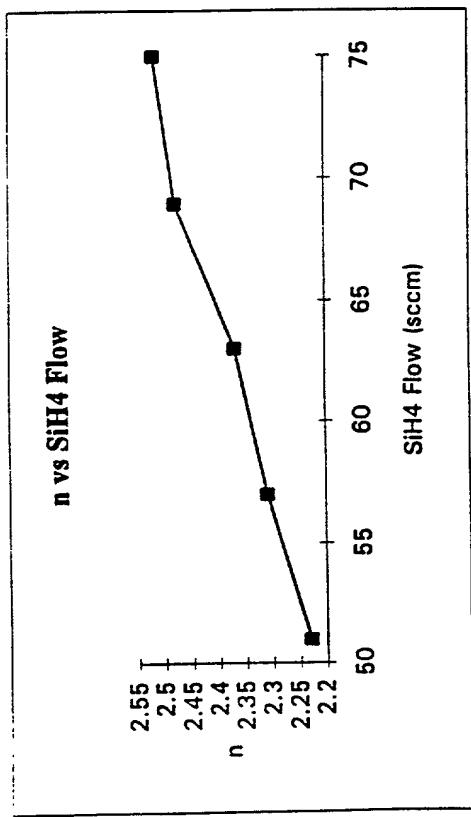
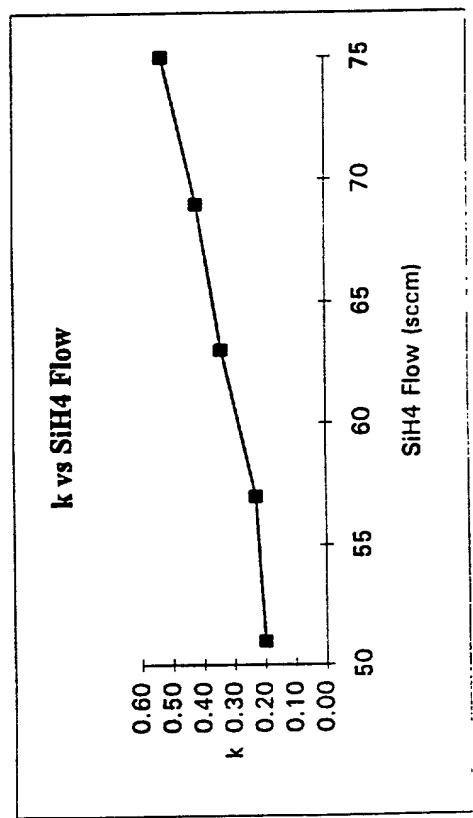
## APPENDIX C

### 200mm ARL Process Trends (300A/356nm)



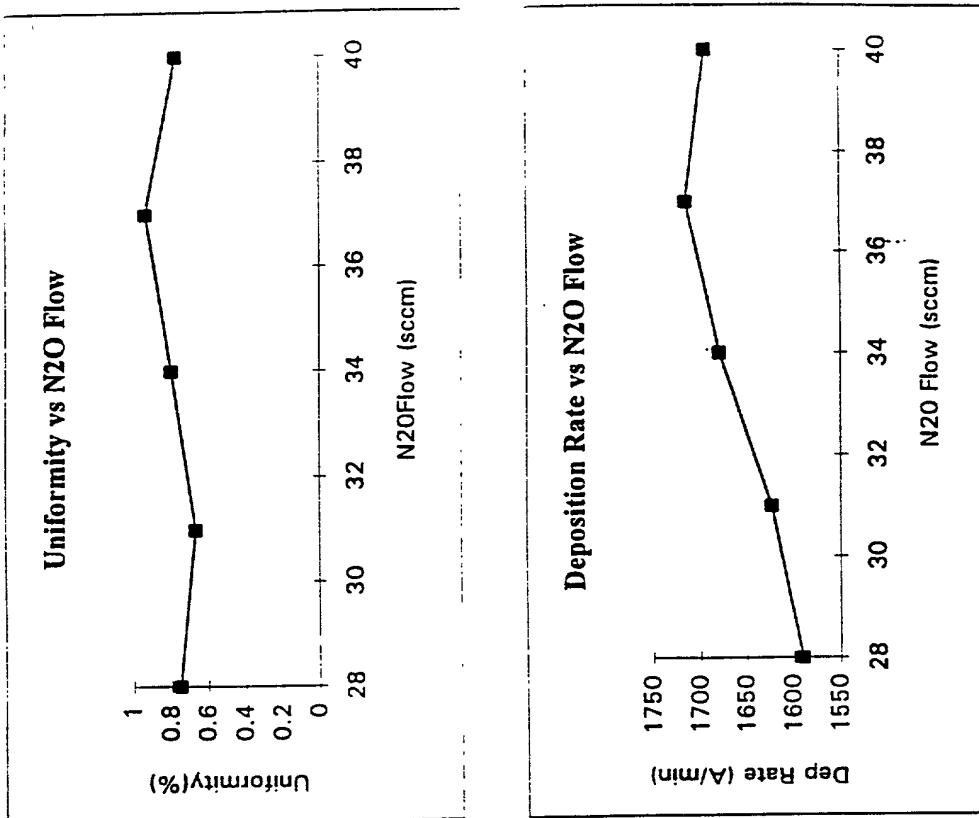
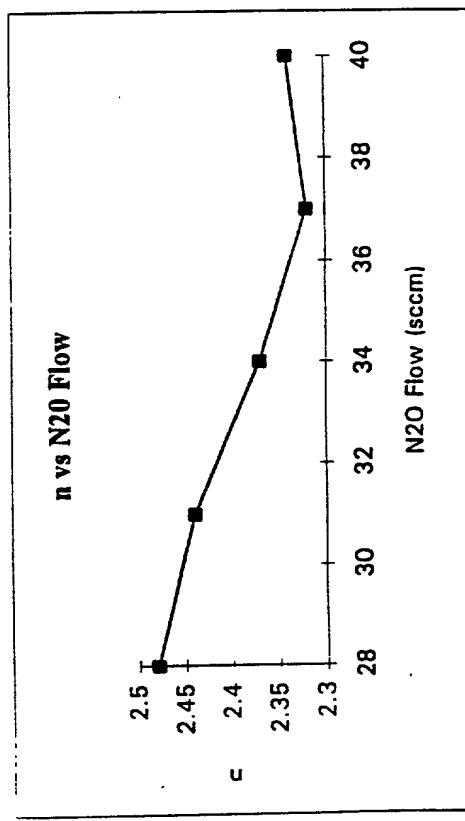
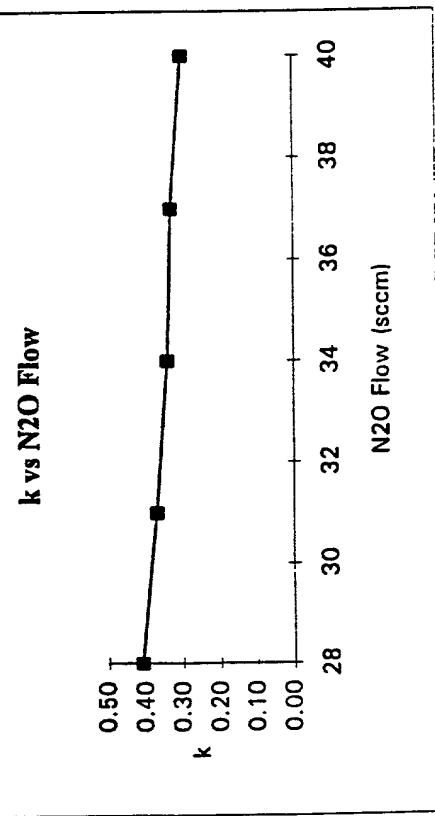
## 200mm ARI Process Trends (300Å/356nm)

### APPENDIX C



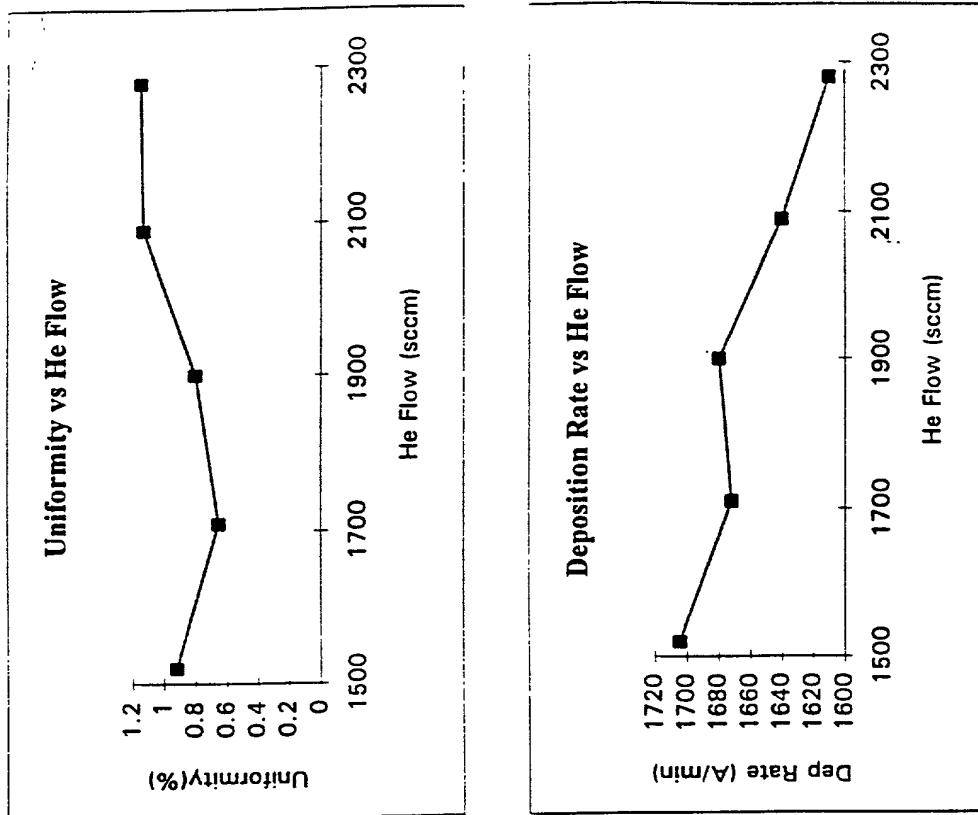
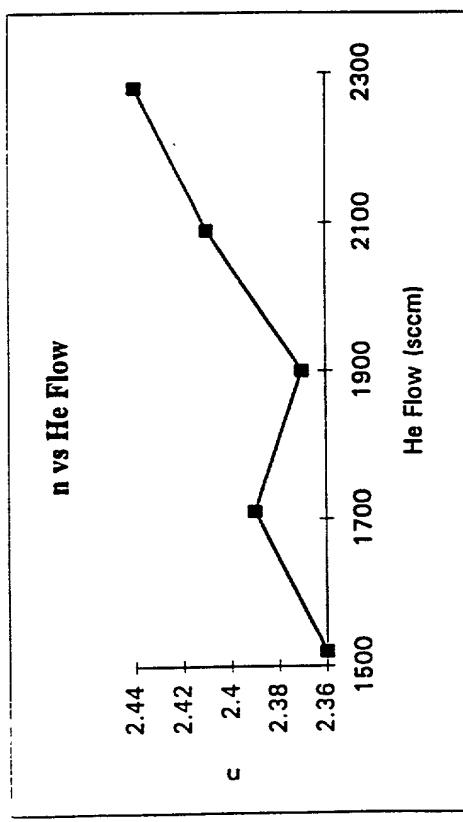
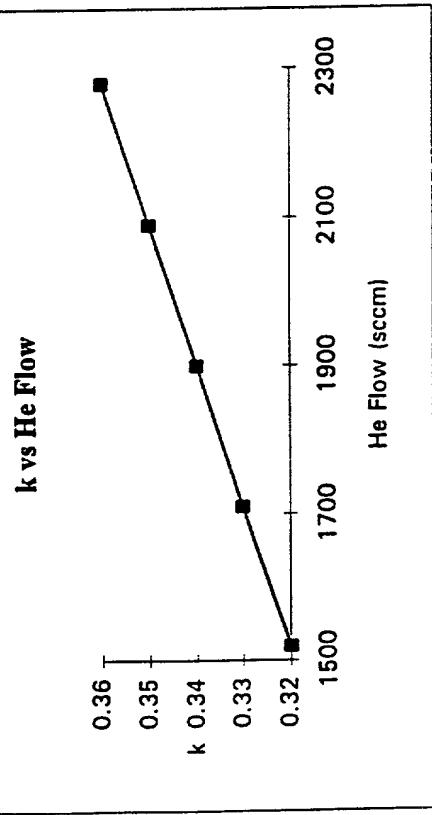
## 200mm ARL Process Trends (300A/356nm)

### APPENDIX C



## APPENDIX C

### 200mm ARL Process Trends (300A/356nm)



## 200mm ARL Process Trends (300A/356nm)

### APPENDIX C

